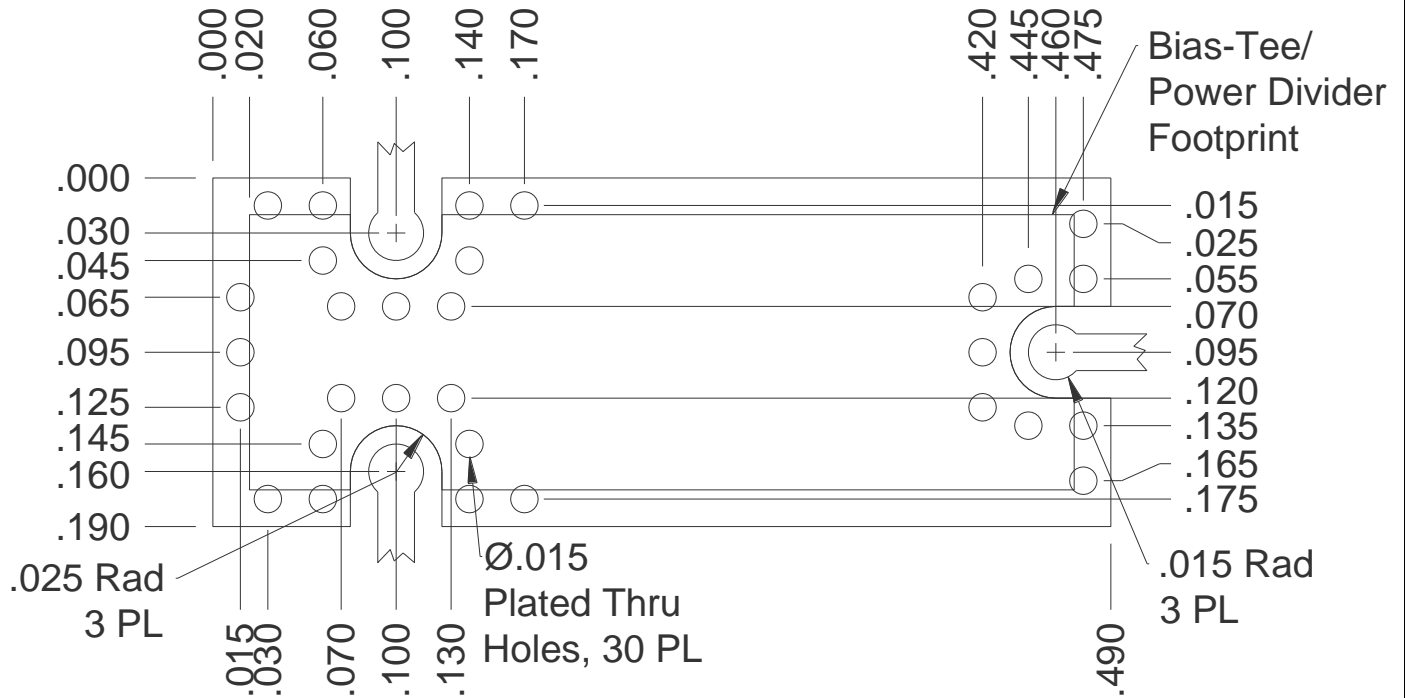




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BTSML/PDLSM Surface-Mount System Circuit Footprint



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