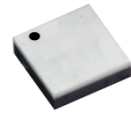


## BROADBAND DISTRIBUTED AMPLIFIER

## ADM-0026-5929SM

The ADM-0026-5929SM is a broadband, efficient GaAs PHEMT distributed amplifier in a 4mm QFN surface mount package. It is designed to provide optimal LO drive for T3 mixers and offers 13 dB typical gain and 20 dBm typical saturated output power for 165 mA of current. Extended high frequency gain offers improved harmonic generation and rise times improving T3 linearity. The ADM-0026-5929SM is biased with a 5-7 volt positive bias and an optional current reducing negative voltage with external bias tee. Additional applications include clock signals and other general purpose driver applications in electronic warfare and test and measurement. The ADM-0026-5929SM is an excellent alternative to competing GaAs PHEMT distributed amplifiers.



### Features

- Optimized for use as a [T3 LO buffer amplifier](#)
- Fast <10 ps risetime
- 3<sup>rd</sup> and 5<sup>th</sup> Harmonic Generation
- Broadband 50  $\Omega$  Matching
- Unconditionally Stable

**Electrical Specifications** - Specifications measured in a 50-Ohm system.

Parameter	Frequency (GHz)	Typ
Input for Saturated Output (dBm)	DC to 26.5	+10
Output 1 dB Compression (dBm)		+15
Saturated Output Power with negative bias (dBm)		+20
Small Signal Gain with negative bias (dB)		13
Return Loss (dB)		14
Noise Figure (dB)		5.5
Third Order Output Intercept Point (dBm)		26
Bias Requirements, External (mA)		165
Vd: +5.0 to +7.0 / Vg: -0.25 Volts Vd: +5.0 to +7.0 / Vg: 0 Volts		225

### Part Number Options

Model Number	Description
ADM-0026-5929SM <sup>1</sup>	Surface Mount 4mm QFN
EVAL-ADM-5929SM	Connectorized Evaluation Fixture

<sup>1</sup>Note: For port locations and I/O designations, refer to the drawings on page 2, 8, and 9 of this document.

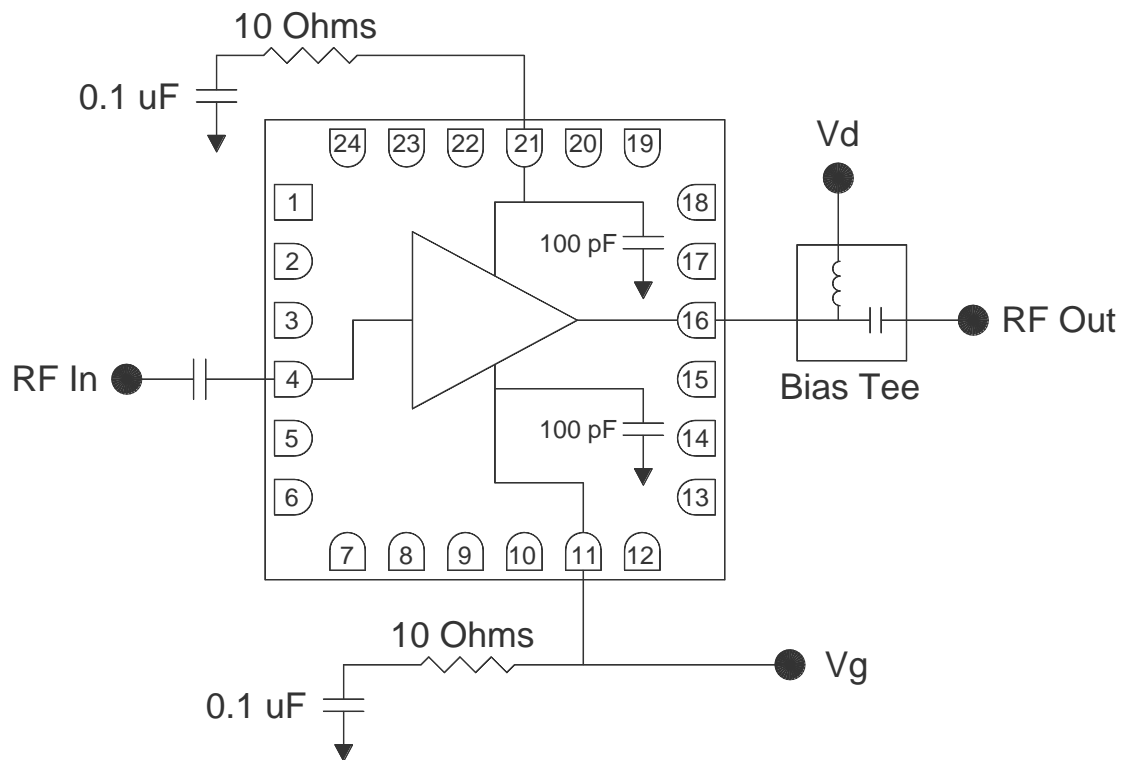
## BROADBAND DISTRIBUTED AMPLIFIER

ADM-0026-5929SM

Page 2

Frequency DC to 26.5 GHz

Functional Diagram and Application Circuit



### Biasing and Operation

**RF In / RF Out** – Input and output signals should be connected by 50 ohm microstrip or coplanar traces to well matched 50 ohm sources and loads. DC blocking capacitors and bias tees are required.

**Vg** – Negative gate voltage is optional, the amplifier can operate with positive bias only. Application of a negative bias between 0 and -0.3V will reduce current consumption, improve even harmonic suppression, and potentially improve T3 linearity. Reduced current consumption can also lead to increased amplifier lifetime. The amplifier is designed for optimal performance when the negative gate voltage is tuned such that current drawn from the *positive* bias supply is 165 mA. It may be supplied through pin 11 or through the RF input on pin 4.

**Vd**- Bias supply supplied to Vd through pin 16 should be voltage limited below 9 V and current limited below 275 mA at all times. The operational bias voltage should be between 5 V and 7 V for full gain, efficiency, and linearity. In general gain, linearity, and output power will increase marginally with increased voltage from 5 to 7 V.

**Optional Bias Circuitry** – The resistor and capacitor on the Vd and Vg lines (pads 21 and 11) prevent low frequency oscillation. These components are not required in bias circuits with sufficient low frequency loss. Designers should experiment to determine if they are necessary.

**DC/RF Ground** – The ground paddle of the QFN should be connected to a low noise RF and DC ground with very low electrical and thermal resistance for high frequency operation and thermal heat sinking.

***GaAs MMIC devices are susceptible to Electrostatic Discharge. Use proper ESD precautions when handling these items.***

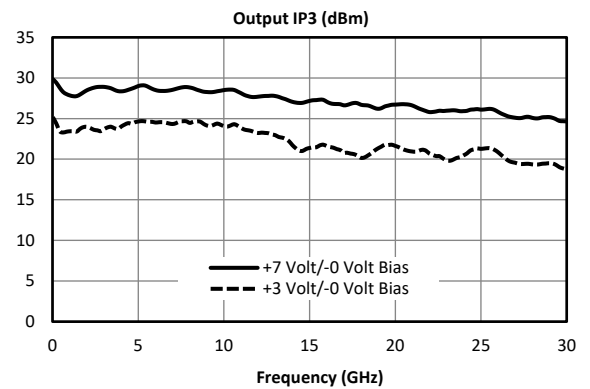
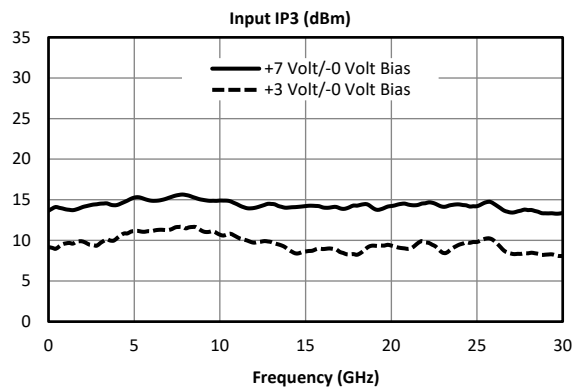
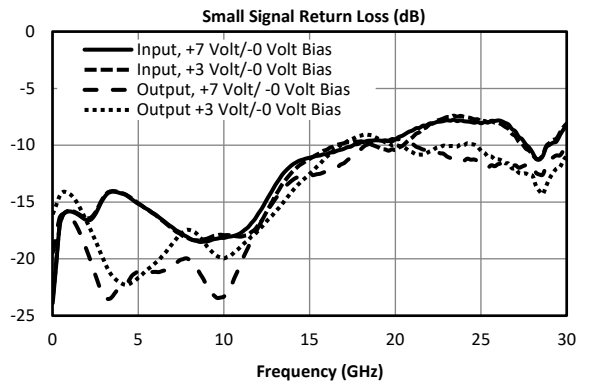
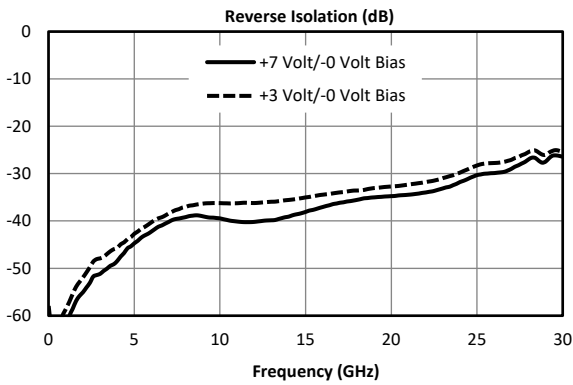
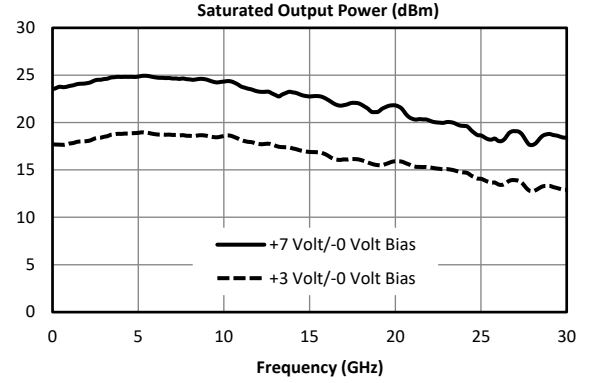
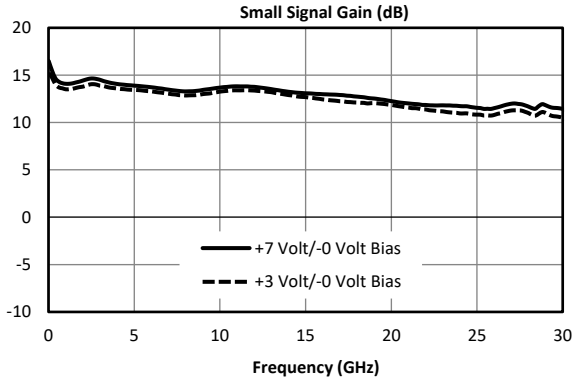
# BROADBAND DISTRIBUTED AMPLIFIER

# ADM-0026-5929SM

Page 3

Frequency DC to 26.5 GHz

Typical Performance: Positive Only (+3 to +7V) Bias (Pin 16 Output), Grounded Gate (Pin 11)



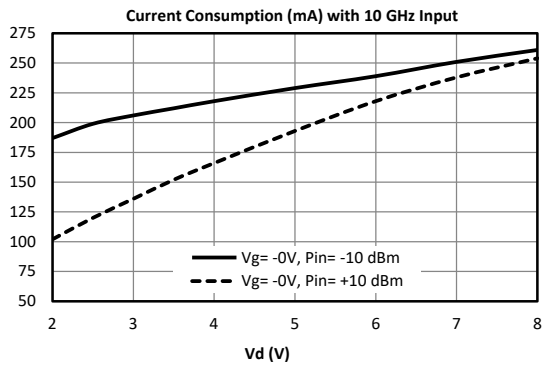
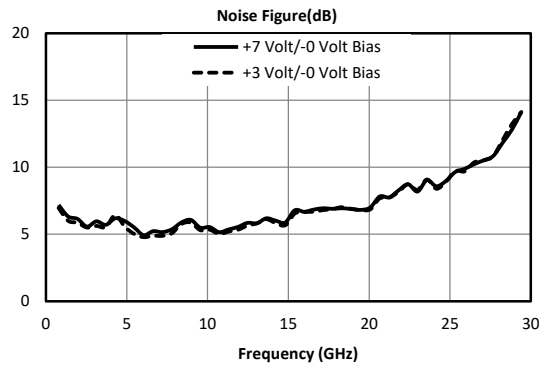
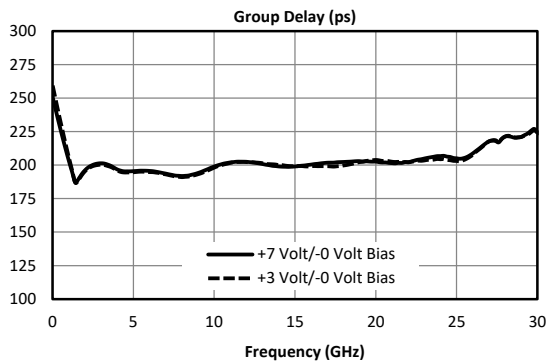
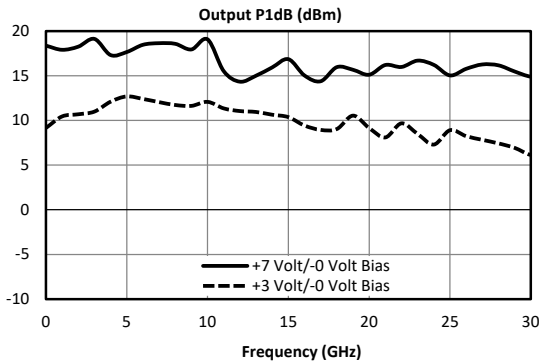
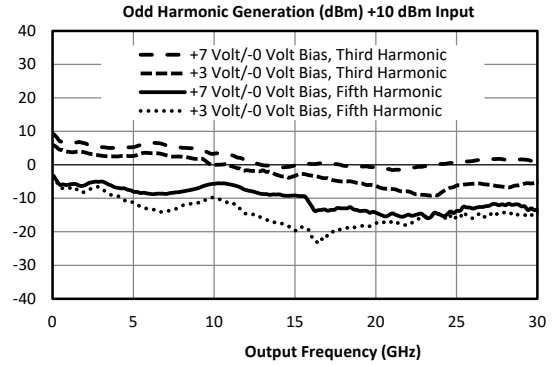
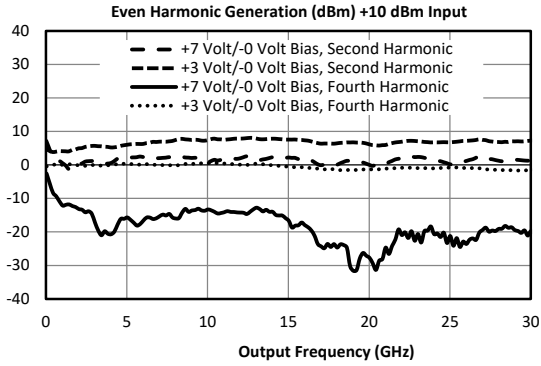
# BROADBAND DISTRIBUTED AMPLIFIER

Page 4

# ADM-0026-5929SM

Frequency DC to 26.5 GHz

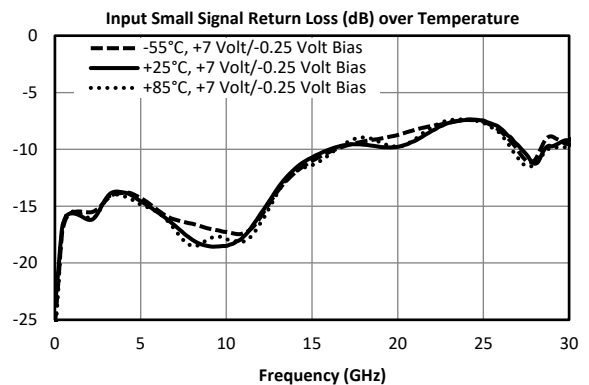
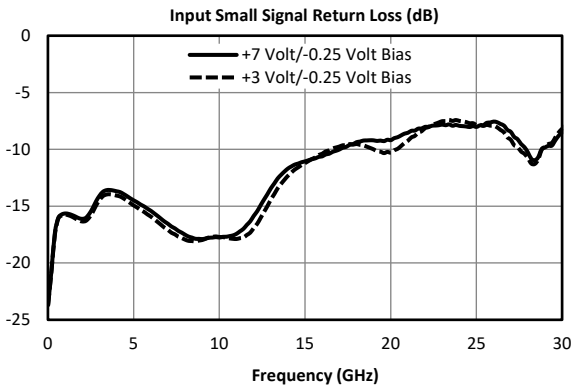
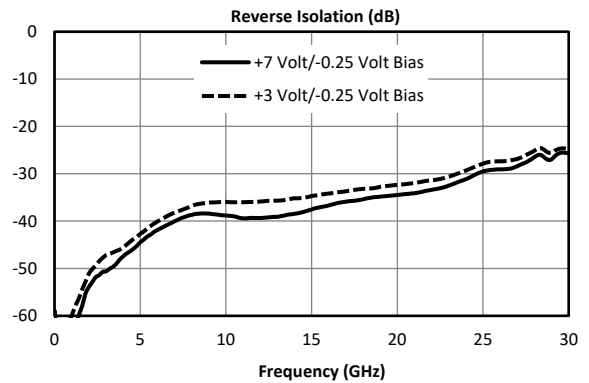
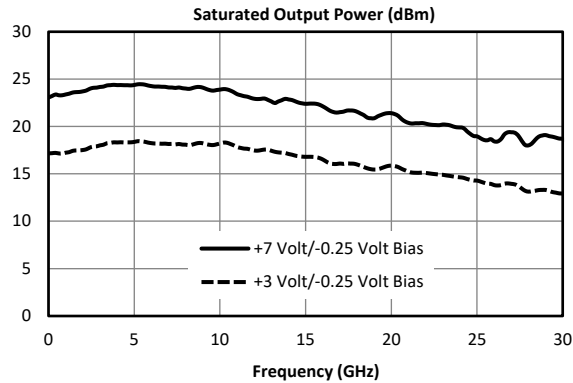
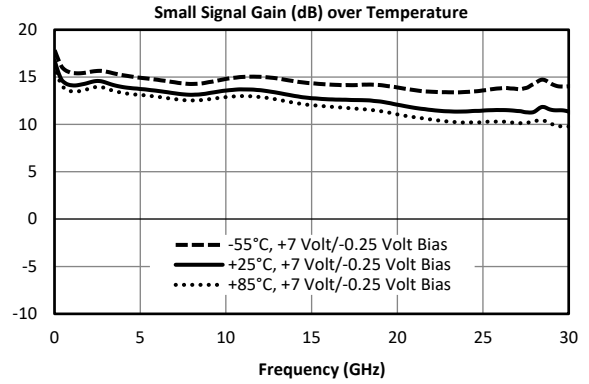
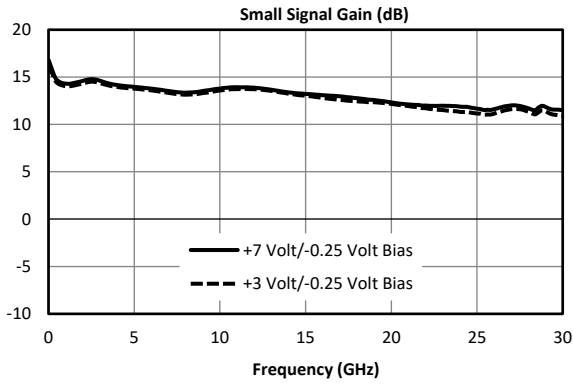
Typical Performance: Positive Only (+3 to +7V) Bias (Pin 16 Output), Grounded Gate (Pin 11), continued



# BROADBAND DISTRIBUTED AMPLIFIER

# ADM-0026-5929SM

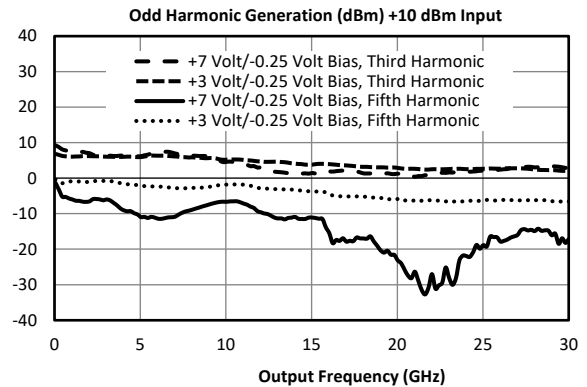
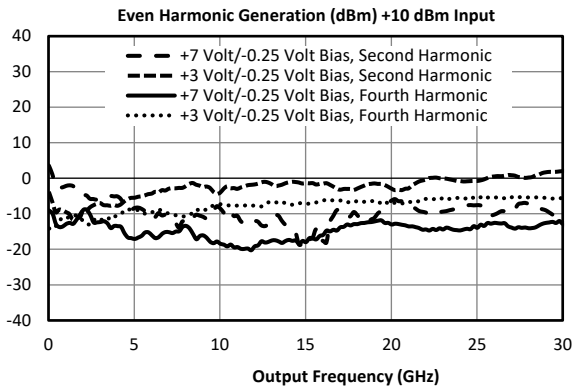
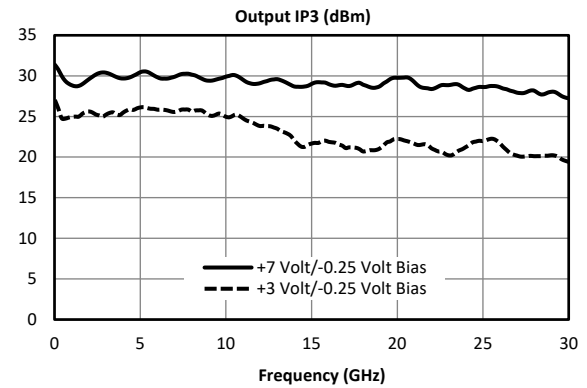
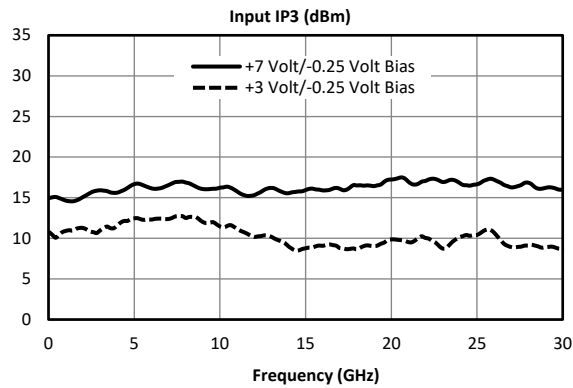
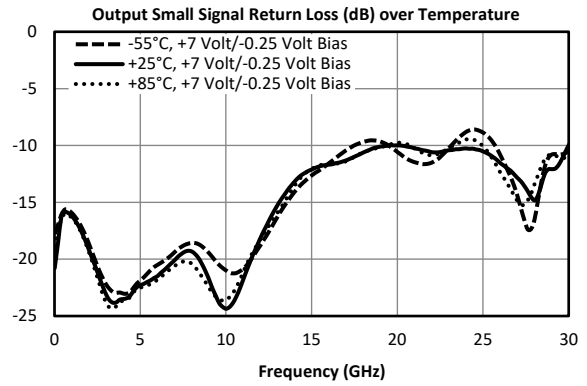
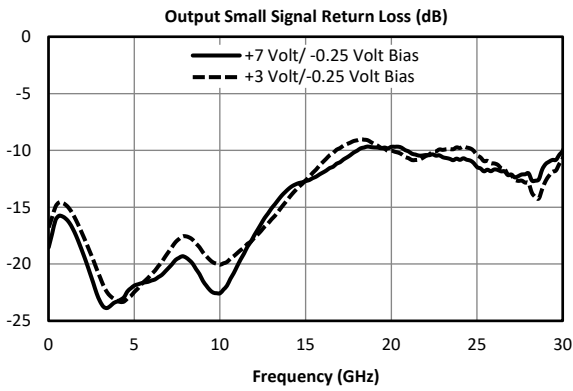
Typical Performance: +5 to +7V Positive Bias (Pin 16 Output), -.25 Negative Bias (Pin11)



# BROADBAND DISTRIBUTED AMPLIFIER

# ADM-0026-5929SM

Typical Performance: +5 to +7V Positive Bias (Pin 16 Output), -.25 Negative Bias (Pin11)



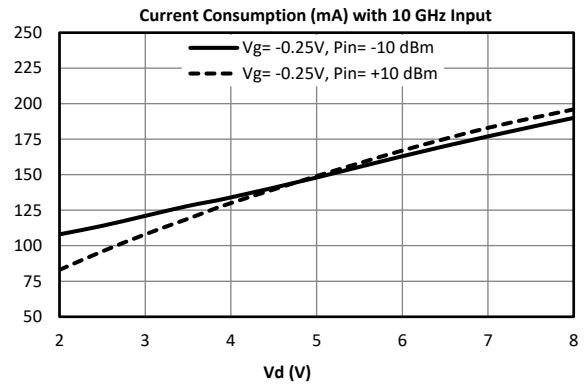
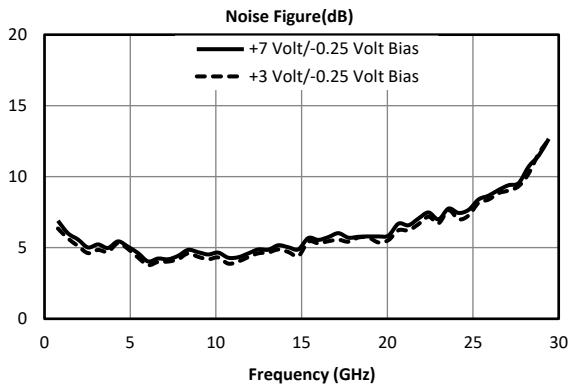
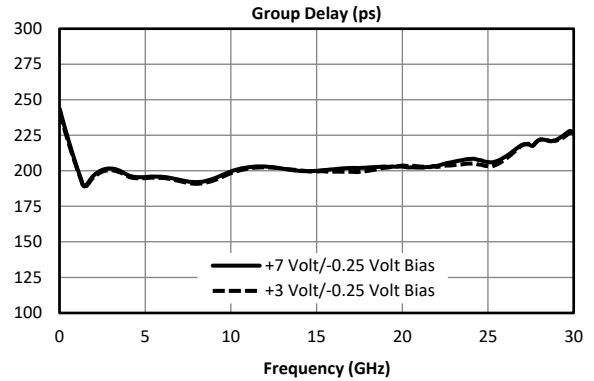
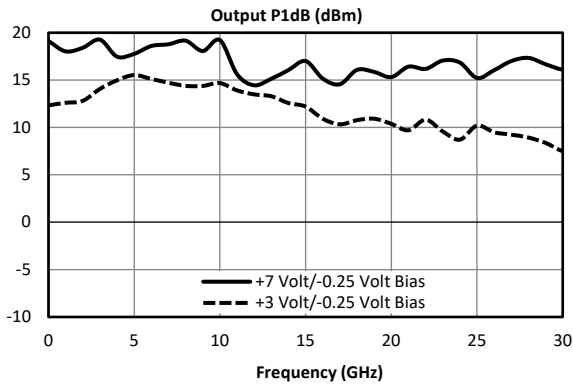
# BROADBAND DISTRIBUTED AMPLIFIER

Page 7

# ADM-0026-5929SM

Frequency DC to 26.5 GHz

Typical Performance: +5 to +7V External Bias (Pin 16 Output), -0.25V Negative Bias (Pin11) continued



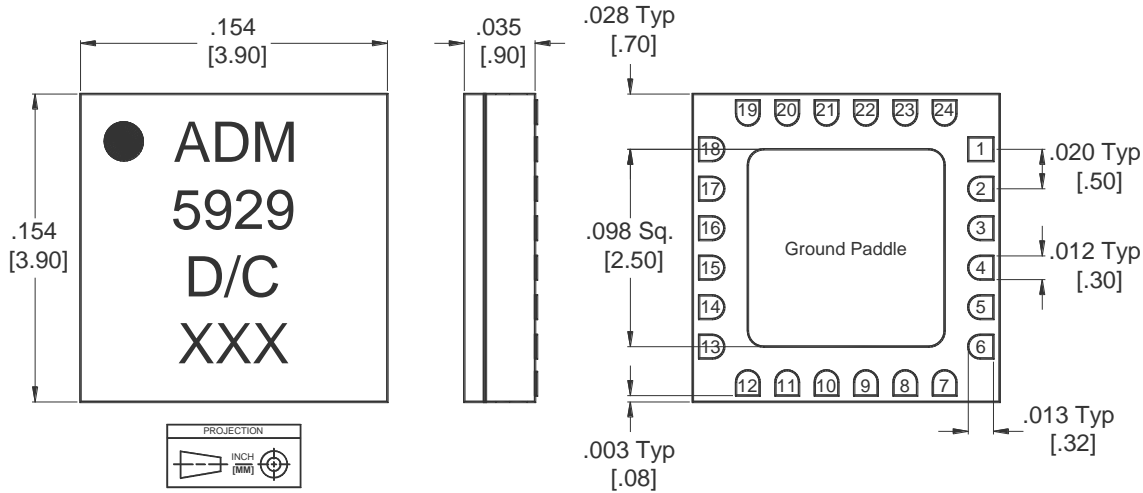
# BROADBAND DISTRIBUTED AMPLIFIER

# ADM-0026-5929SM

Page 8

Frequency DC to 26.5 GHz

Outline Drawing



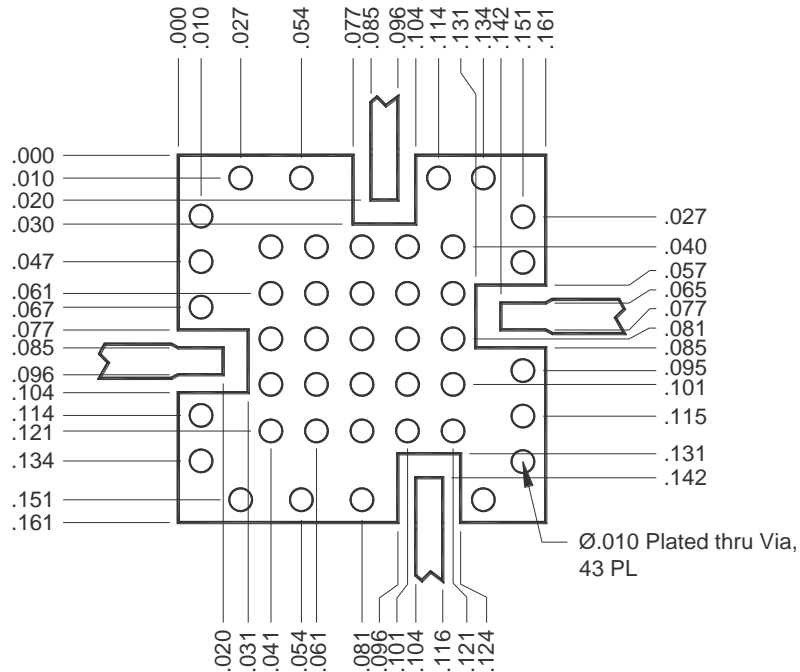
Substrate material is Ceramic.

I/O Leads and Die Paddle are:

- 0.03-0.254 microns Gold, over
- 0.07-0.17 microns Palladium, over
- 1.27 to 8.89 microns Nickel.

All unconnected pads should be connected to PCB RF ground.

PCB Footprint Drawing



QFN-Package Surface-Mount Landing Pattern

[Click here for a DXF of the above layout.](#)

[Click here for leaded solder reflow.](#) [Click here for lead-free solder reflow.](#)

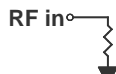
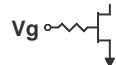





# BROADBAND DISTRIBUTED AMPLIFIER

# ADM-0026-5929SM

### Minimum Performance Specs of ADM-0026-5929SM

Parameter	Frequency (GHz)	Min	Units
Saturated Output Power (dBm)	DC to 10	+21	dBm
	10 to 20	+19	
Small Signal Gain (dB)	DC to 10	11	dB
	10 to 20	9	

Pin Descriptions			
Pin Number	Function	Description	Interface Schematic
1-3, 5-10, 12-15, 17-20, 22, 24	NC	These pins are not connected internally. Datasheet performance is tested with NC pins grounded.	
4	RF in	This pin is DC coupled and matched to 50 Ω.	
11	Vg	Gate control for the amplifier. External decoupling resistor/capacitor is required.	
16	RF out / Vd	This pad is DC coupled and matched to 50 Ω.	
21	Vd2	External decoupling resistor/capacitor is required.	
Paddle	GND	Ground pad should be connected to RF/DC ground with low electrical and thermal resistance.	

Absolute Maximum Ratings	
Parameter	Maximum Rating
Positive Bias Voltage	9 V
Positive Bias Current	275 mA
Negative Bias Voltage	-2 V
Negative Bias Current	2 mA
RF Input Power	+20 dBm
Power Dissipation	2 W
Thermal Resistance, $\theta_{jc}$	67 C/W
Max Junction Temperature to Maintain 10 <sup>6</sup> hours Mean Time to Failure (MTTF):	175°C
ESD (Human Body Model)	Class 0
Operating Temperature	-55°C to +85°C
Storage Temperature	-65°C to +150°C

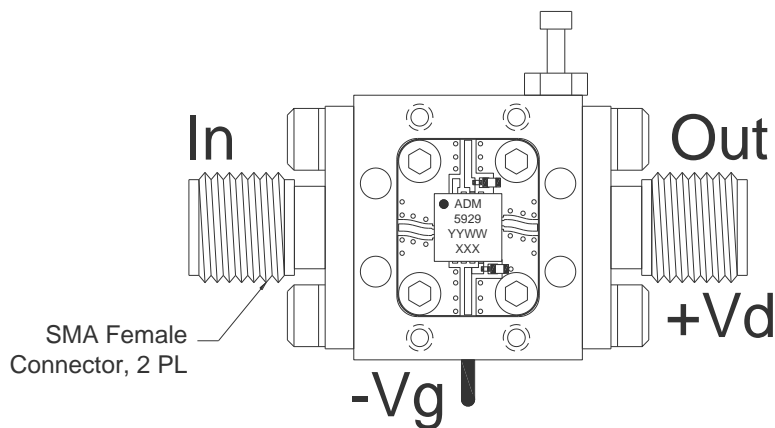
# BROADBAND DISTRIBUTED AMPLIFIER

**ADM-0026-5929SM**

Page 10

Frequency DC to 26.5 GHz

Evaluation Board



The evaluation module follows Marki standard assembly and evaluation procedures to give optimal performance for datasheet characterization. Actual QFN performance will depend on substrate material, bypass capacitors, resistors, connectors, quality of bias current/voltage source, and assembly process.

Evaluation Board Bill of Materials	
Item	Description/Part Number
Connectors	Southwest 214-510SF
Bias Pins	Kovar
Housing	Aluminum
Circuit	.008 Thick Rogers 4003
0.1 uF Capacitor	AVX 0402YD104KAT2A
10 Ω Resistor	Venkel CR0201-20W-100JT
ADM 5929	ADM-0026-5929SM

DATA SHEET NOTES:

- Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.  
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Revision History

Revision Code	Revision Date	Comment
-	2015	Datasheet Initial Release
A	May 2019	Updated minimum specifications
B	July 2019	Corrected Thermal Resistance
C	October 2019	Plating call-out changed to ENEPIG effective starting on D/C 1846
D	December 2020	Added Max Junction Temp specification