



TWO-TONE-TERMINATOR MIXER/LO-AMPLIFIER

T3A-07SMG

The T3A-07SMG is a lead-free, versatile, robust, broadband Two-Tone-Terminator mixer integrated with a fast rise time square wave amplifier. The T3A-07SMG employs the most sophisticated mixer on the market today and offers unparalleled performance when compared to other mixer technologies. The T3A-07SMG delivers exceptional IMD suppression with low conversion loss, offering superior IP3 as compared to discrete amplifier/mixer combinations.

Features

- Ultra-Broadband Integrated RF, LO, and IF Baluns
- Integrated Square-Wave LO Amplifier
- Low Minimum LO Drive
- Industry Leading Spurious, IP3, and P_{1dB} Performance
- Application Note: [T3 Mixer Primer](#)
- RoHS Compliant

Electrical Specifications - Specifications guaranteed from -30 to +70°C, measured in a 50Ω system.

Parameter	LO (GHz)	RF (GHz)	IF (GHz)	Min	Typ	Max
Conversion Loss (dB)	.01-7 .01-7	.01-7 .01-7	.001-0.5 .001-4.0		6.5 7.5	10.0 11.0
Saturated LO Drive Level (dBm)				+10		+15
Linear LO Drive Level (dBm)				-1		+5
LO Leakage (dBm) LO-RF LO-IF	.01-7	.01-7			See Plots	
RF-IF Isolation (dB)	.01-7	.01-7			See Plot	
Input 1 dB Compression (dBm)	.01-7	.01-7			+17	
Input Two-Tone Third Order Intercept Point (dBm)	.01-7	.01-7			See Plot	
Bias Requirements (mA) ¹ +5.0 Volts DC (+7 V max) -0.2 Volts DC					160 0	200 1

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
T3A-07SMG-1 ¹	Surface Mount IF Configuration -1	SM	RoHS	Pre-release	EAR99
T3A-07SMG-2 ¹	Surface Mount IF Configuration -2	SM			
Eval-T3A-07	Connectorized Evaluation Fixture	Eval			

¹See -2 and -1 I-port configuration options on page 4.

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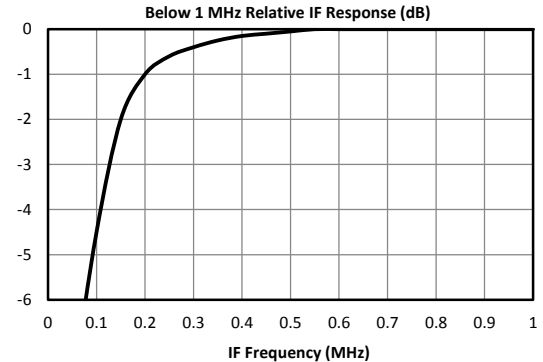
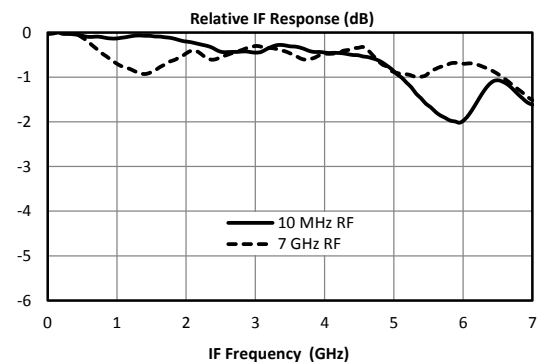
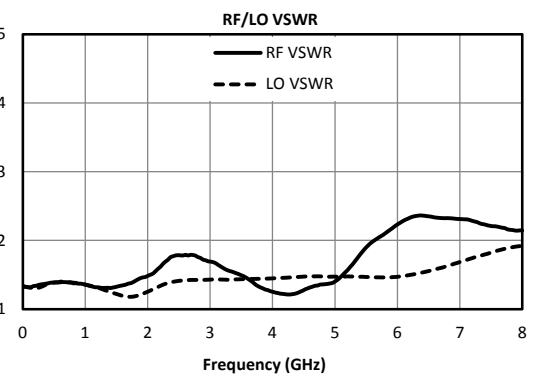
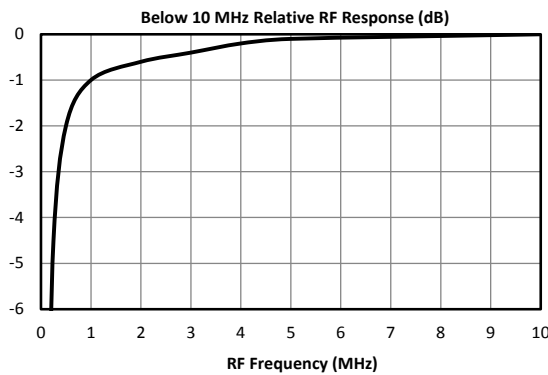
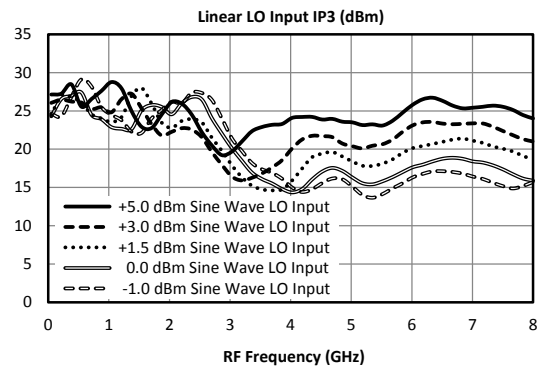
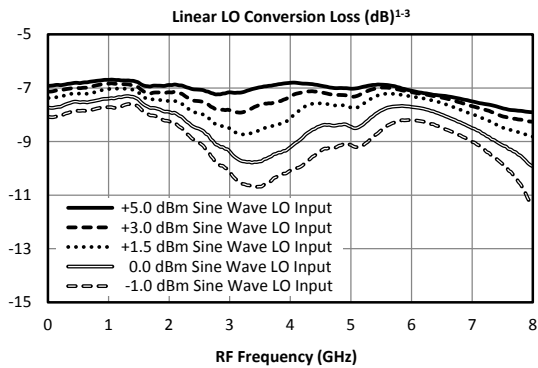
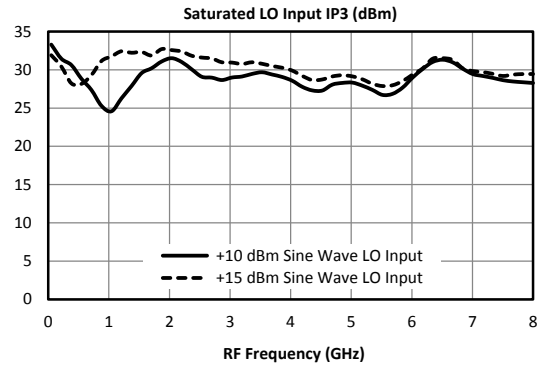
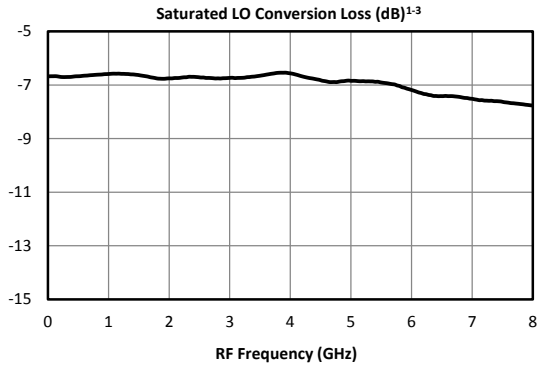
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LO/RF 10 MHz to 7 GHz
IF 1 MHz to 4 GHz

Typical Performance

Unless otherwise specified, data is taken with +10 dBm LO, 5.0V Positive Bias, -0.2 Negative Bias



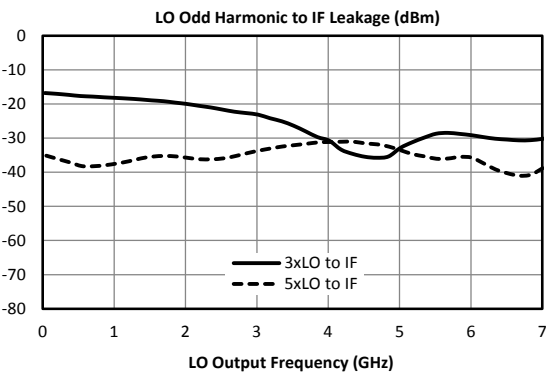
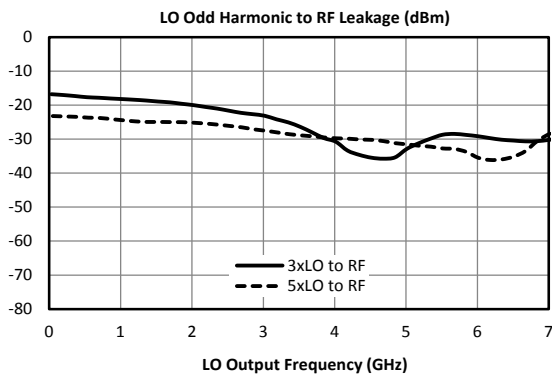
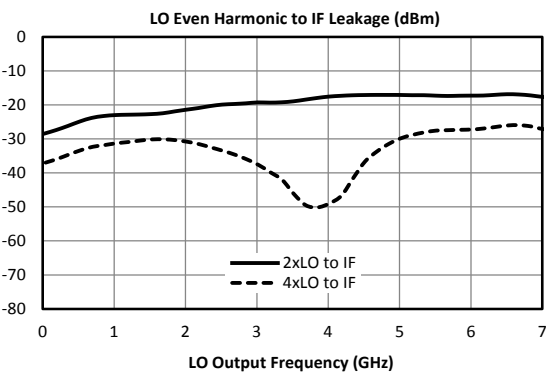
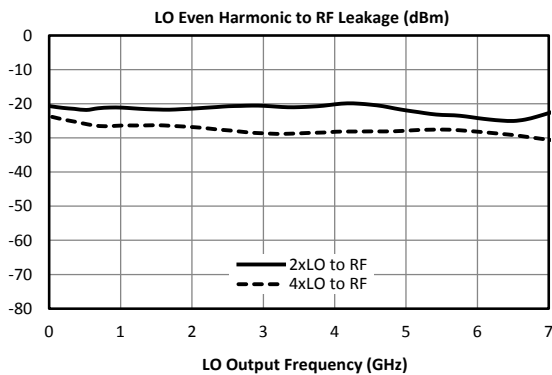
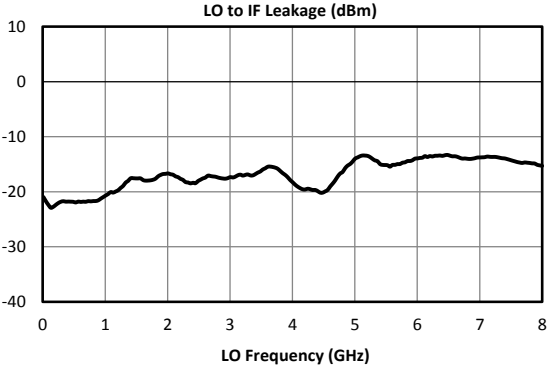
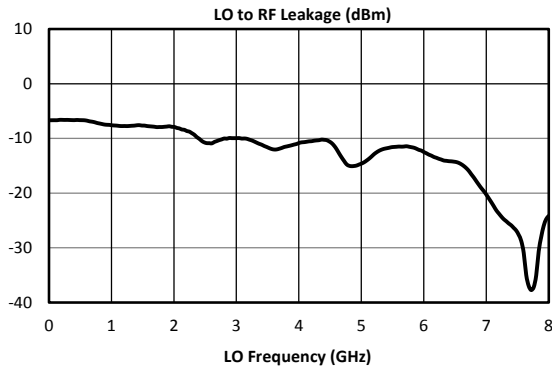
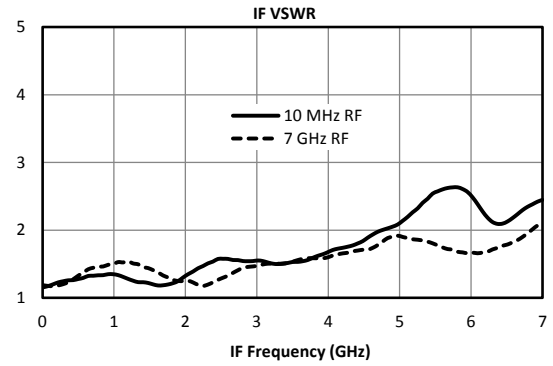
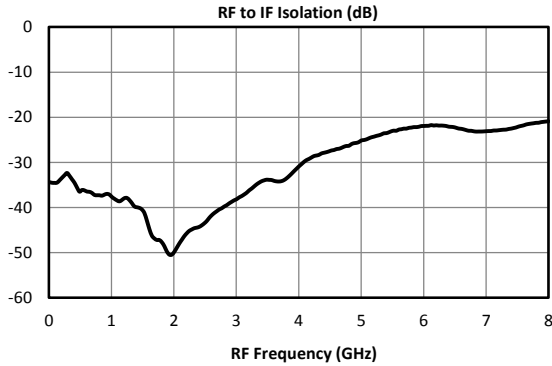
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LO/RF 10 MHz to 7 GHz
IF 1 MHz to 4 GHz

Typical Performance

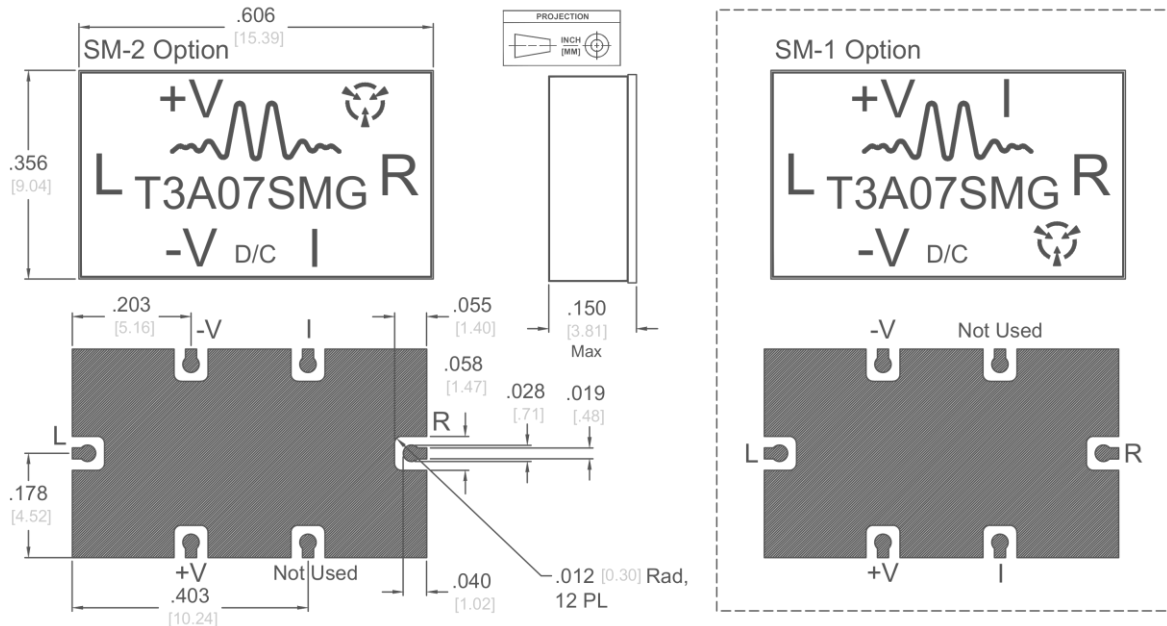


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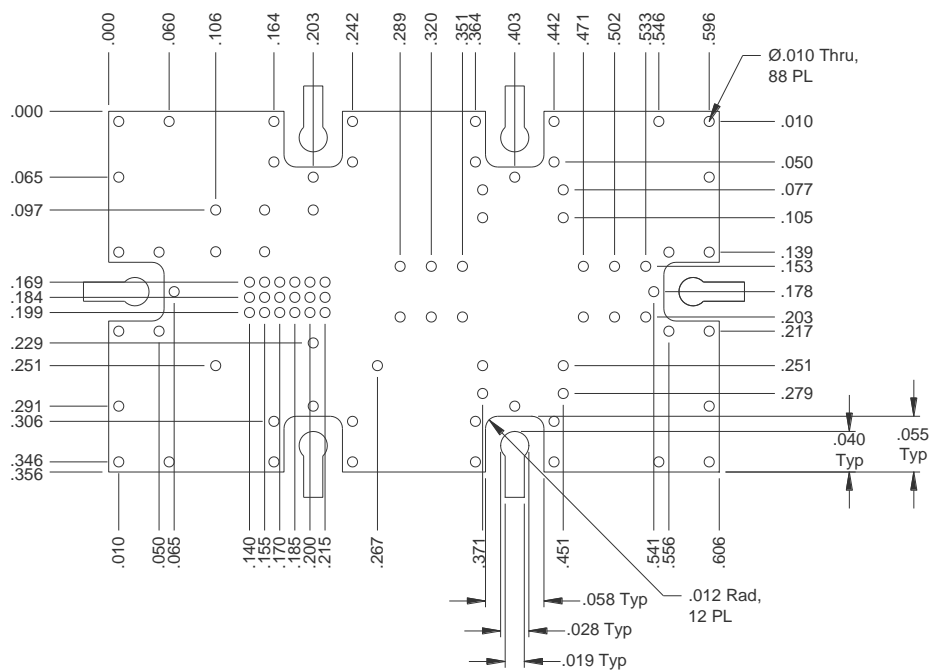
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LO/RF 10 MHz to 7 GHz
IF 1 MHz to 4 GHz



I/O Connections & Ground Plane Finish is Gold Flash, 5 to 10 μ -inches, over Solderable Electroless Nickel, 100-200 μ -inches, over Cu.
T3A-SM package is not gross leak sealed. Do not run PCB through solvent cleaning process post-reflow.
No-clean flux processing is required.

Outline Drawing – SMG-2 and SMG-1 Packages



SMG-Package Landing Pattern

[Click here for a DXF of the above layout.](#)

[Click here for leaded solder reflow.](#) [Click here for lead-free solder reflow.](#)

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**LO/RF 10 MHz to 7 GHz
IF 1 MHz to 4 GHz**

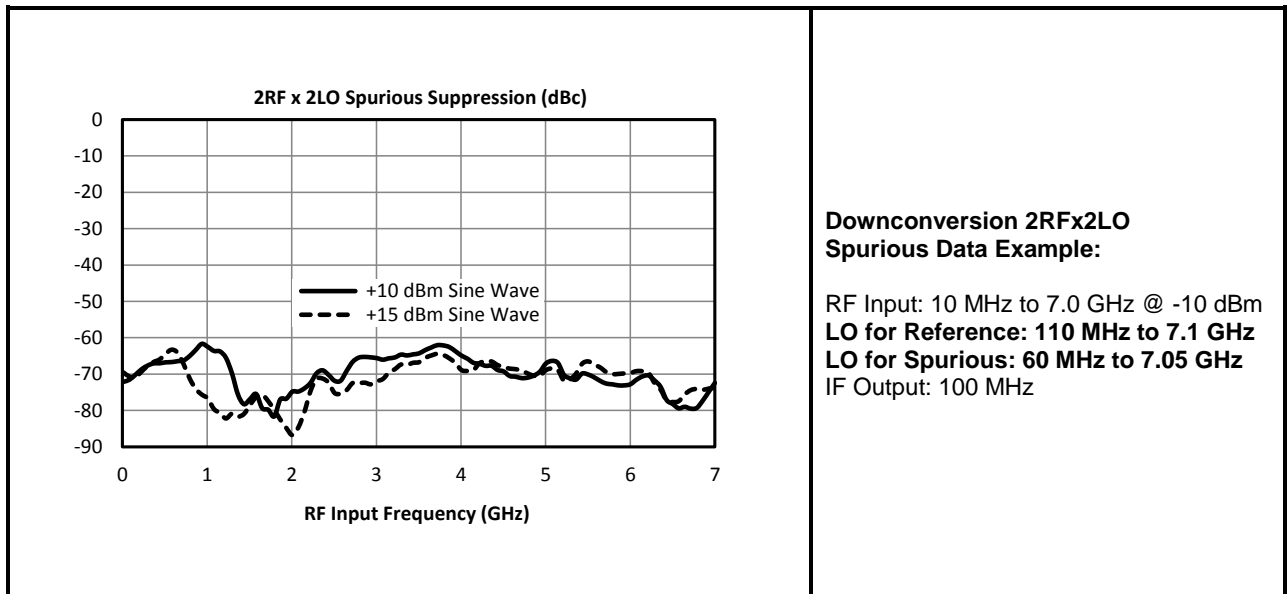
Downconversion Spurious Suppression

Spurious data is taken by selecting RF and LO frequencies ($\pm mLO \pm nRF$) within the 10 MHz to 7 GHz RF/LO bands, which create a 100 MHz IF spurious output. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by $(n-1)$, where "n" is the RF spur order. For example, the 2RFx2LO spur is 72 dBc for a -10 dBm input, so a -20 dBm RF input creates a spur that is $(2-1) \times (-10 \text{ dB})$ dB lower, or 82 dBc.

Typical Downconversion Spurious Suppression (dBc): +10 (+15) dBm Sine Wave LO

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
0xRF	-----	See LO to IF Isolation and LO Harmonic to IF Isolation Plots (Page 3)				
1xRF	23 (25)	Reference	32 (21)	10 (10)	37 (20)	17 (16)
2xRF	74 (74)	72 (72)	70 (72)	73 (73)	67 (70)	70 (74)
3xRF	101 (102)	89 (91)	95 (95)	83 (89)	92 (96)	82 (89)
4xRF	>110	>110	>110	>110	>110	>110
5xRF	>120	>120	>120	>120	>120	>120

A sample downconversion spurious sweep is shown below. An LO 100 MHz higher than the RF is used to create a 100 MHz reference IF. A second LO is used to create a 2x2 spurious IF, also at 100 MHz (50 MHz fundamental IF). The difference between these two output levels is the spurious suppression in dBc. The mean value across the full 10 MHz to 7 GHz RF input band is the number shown in the table above.



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**LO/RF 10 MHz to 7 GHz
IF 1 MHz to 4 GHz**

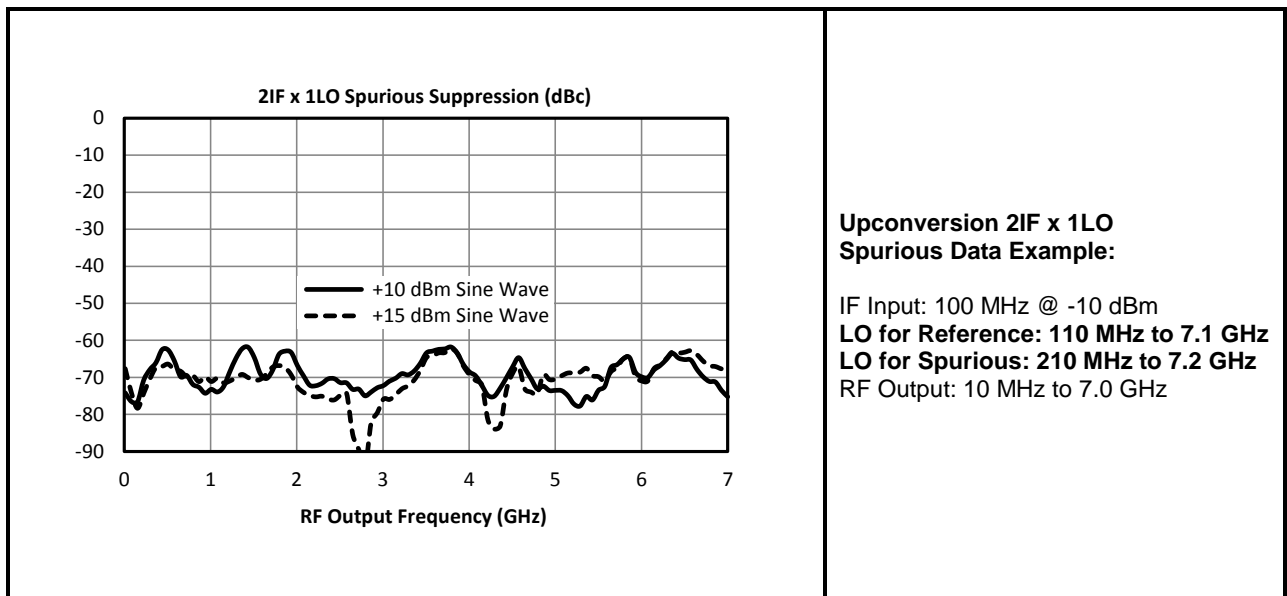
Upconversion Spurious Suppression

Spurious data is taken by mixing a 100 MHz IF with LO frequencies ($\pm mLO \pm nIF$), which creates an RF within the 10 MHz to 7 GHz RF band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by $(n-1)$, where "n" is the IF spur order. For example, the 2IFx1LO spur is typically 69 dBc for a -10 dBm input, so a -20 dBm IF input creates a spur that is $(2-1) \times (-10 \text{ dB})$ dB lower, or 79 dBc.

Typical Upconversion Spurious Suppression (dBc): +10 (+15) dBm Sine Wave LO

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
0xIF	-----	See LO to RF Isolation and LO Harmonic to RF Isolation Plots (Page 3)				
1xIF	27 (29)	Reference	29 (21)	11 (10)	34 (21)	17 (16)
2xIF	74 (78)	69 (71)	70 (75)	66 (71)	68 (74)	66 (72)
3xIF	102 (103)	86 (92)	95 (98)	82 (91)	94 (96)	81 (89)
4xIF	>110	>110	>110	>110	>110	>110
5xIF	>120	>120	>120	>120	>120	>120

A sample upconversion spurious sweep is shown below. A 100 MHz reference IF input is used to create an RF output that is 100 MHz below the LO input ($LO-IF=RF$). A second LO (100 MHz higher) is combined with the same 100 MHz IF input ($LO-2xIF=RF$) to create the same 10 MHz to 7 GHz RF output band. The difference between these two output levels is the spurious suppression in dBc. The mean value across the full RF output band is the number shown in the table above.

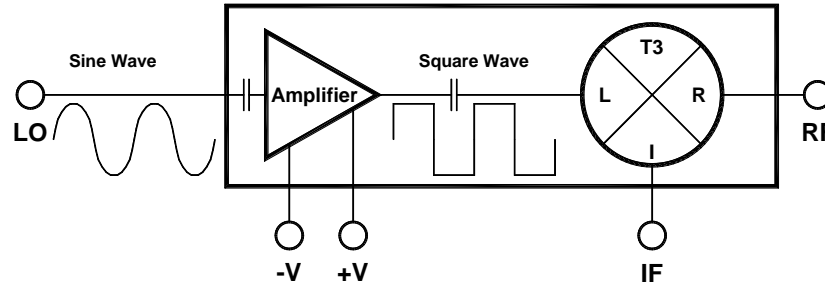





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LO/RF 10 MHz to 7 GHz
IF 1 MHz to 4 GHz



Port	Description	DC Interface Schematic
LO	The LO port is DC blocked and AC matched to 50 Ohms from 10 MHz to 7 GHz.	
RF	The RF port is DC short to ground and AC matched to 50 Ohms from 10 MHz to 7 GHz. Blocking capacitor is optional.	
IF	The IF port is DC blocked and AC matched to 50 Ohms from 1 MHz to 4 GHz.	

Absolute Maximum Ratings	
Parameter	Maximum Rating
RF DC Current	1 Amp
LO DC Current	N/A
RF Power Handling	+25 dBm
LO Power Handling	+17 dBm
Operating Temperature	-30°C to +70°C
Storage Temperature	-65°C to +125°C
ESD Sensitivity (HBM)	Class 0

DATA SHEET NOTES:

- Mixer Conversion Loss Plot IF frequency is 100 MHz.
- Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz .
- Conversion Loss typically degrades less than 0.5 dB at +100°C and improves less than 0.5 dB at -55°C.
- Unless otherwise specified, data is taken with +10 dBm LO, 5.0V Positive Bias, -0.2 Negative Bias.
- Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.
- Catalog mixer circuits are continually improved. Configuration control requires custom mixer model numbers and specifications.



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**LO/RF 10 MHz to 7 GHz
IF 1 MHz to 4 GHz**

Revision History

Revision code	Revision Date	Comment
-	October 2017	Datasheet initial Release
A	March 2019	Cleaning Process Warning Added

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