

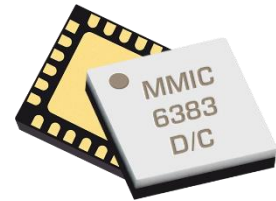
GaAs MMIC Double Balanced Mixer

MM1-0212HSM

1. Device Overview

1.1 General Description

The MM1-0212HSM is a GaAs MMIC double balanced mixer that is optimized for low frequency applications. MM1-0212HSM is a low frequency S band mixer that works well as both an up and down converter through X band. This mixer offers low conversion loss and high LO to RF isolations at moderate LO powers. The sister MM1-0212SSM is recommended for applications which demand higher linearity. The MM1-0212LSM is recommend for low power applications. The MM1-0212HSM is available in a 4x4 mm QFN package. Evaluation boards are available. For a list of recommended LO driver amps for all mixers and IQ mixers, see [here](#).



QFN

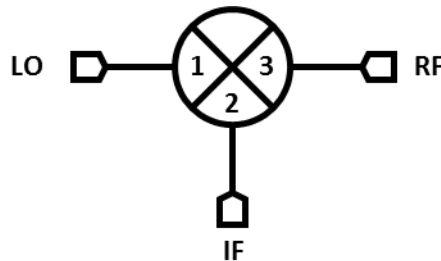
1.2 Features

- High LO to RF isolation
- RoHS Compliant

1.3 Applications

- Test and measurement equipment

1.4 Functional Block Diagram



1.5 Part Ordering Options¹

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
MM1-0212HSM-2	4x4 mm QFN	SM	RoHS	Active	EAR99
EVAL-MM1-0212H	Connectorized Evaluation Fixture	Eval		Active	EAR99

¹ Refer to our [website](#) for a list of definitions for terminology presented in this table.

Table of Contents

1. Device Overview	1	3.6 Typical Performance Plots	6
1.1 General Description	1	3.6.1 Typical Performance Plots: IP3 ..	8
1.2 Features	1	3.6.2 Typical Performance Plots: LO	
1.3 Applications	1	Harmonic Isolation.....	9
1.4 Functional Block Diagram	1	3.6.3 Typical Spurious Performance:	
1.5 Part Ordering Options.....	1	Down-Conversion	10
2. Port Configurations and Functions	3	3.6.4 Typical Spurious Performance: Up-	
2.1 Port Diagram	3	Conversion	10
2.2 Port Functions	3	4. Operation.....	11
3. Specifications	4	4.1 Application Circuit	11
3.1 Absolute Maximum Ratings.....	4	4.2 Ports Operation	11
3.2 Package Information	4	5. Mechanical Data.....	12
3.3 Recommended Operating Conditions .	4	5.1 SM Package Outline Drawing.....	12
3.4 Sequencing Requirements	4	5.2 SM Package Footprint	12
3.5 Electrical Specifications	5	5.3 Evaluation Board Outline Drawing...	13

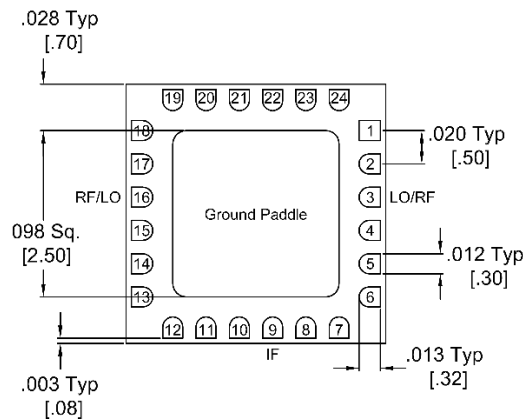
Revision History

Revision Code	Revision Date	Comment
-	July 2018	Datasheet Initial Release
A	November 2018	Section 5.1, Note 2
B	January 2019	Added max power/current spec, ESD rating

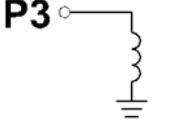
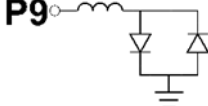
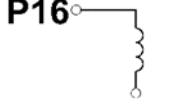
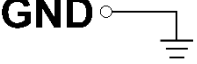
2. Port Configurations and Functions

2.1 Port Diagram

A bottom-up view of the MM1-0212HSM's SM package outline drawing is shown below. The MM1-0212HSM has the input and output ports given in Port Functions. The MM1-0212HSM can be used in either an up or down conversion. For configuration A, input the LO into pin 3, use pin 16 for the RF, and port 9 for the IF. For configuration B, input the LO into pin 16, use pin 3 for the RF, and pin 9 for the IF.



2.2 Port Functions

Port	Function	Description	Equivalent Circuit for Package
Pin 3	LO (Configuration A) RF (Configuration B)	Pin 3 is DC short and AC matched to 50 Ohms from 2 to 12 GHz. Blocking capacitor is optional.	
Pin 9	IF	Pin 9 is DC coupled to the diodes. Blocking capacitor is optional.	
Pin 16	RF (Configuration A) LO (Configuration B)	Pin 16 is DC open and AC matched to 50 Ohms from 2 to 12 GHz. Blocking capacitor is optional.	
GND	Ground	SM package ground path is provided through the ground paddle.	

3. Specifications

3.1 Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may be inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Units
Pin 3 DC Current	30	mA
Pin 9 DC Current	30	mA
Power Handling, at any Port	+30	dBm
Operating Temperature	-55 to +100	°C
Storage Temperature	-65 to +125	°C

3.2 Package Information

Parameter	Details	Rating
ESD	Human Body Model (HBM), per MIL-STD-750, Method 1020	1A
Weight	EVAL package	13.4 g

3.3 Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

	Min	Nominal	Max	Units
T _A , Ambient Temperature	-55	+25	+100	°C
LO Input Power	+13		+17	dBm

3.4 Sequencing Requirements

There is no requirement to apply power to the ports in a specific order. However, it is recommended to provide a 50Ω termination to each port before applying power. This is a passive diode mixer that requires no DC bias.

3.5 Electrical Specifications

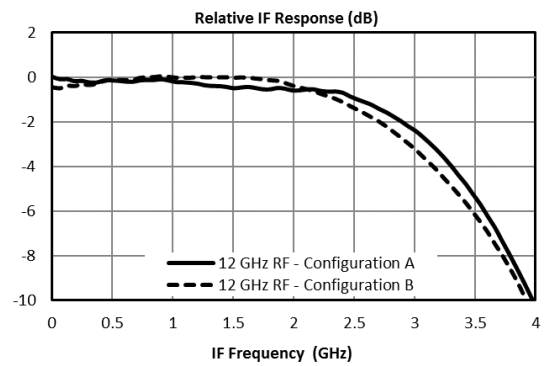
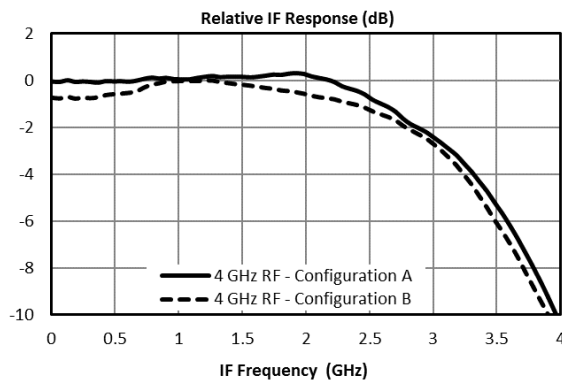
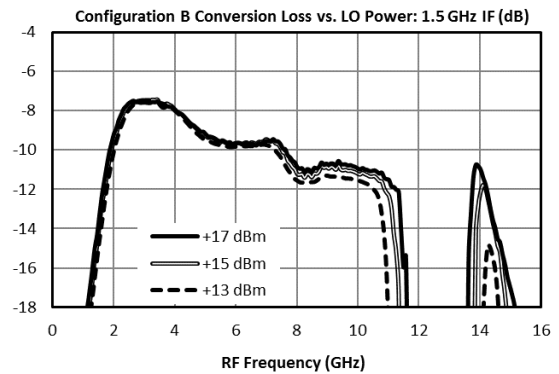
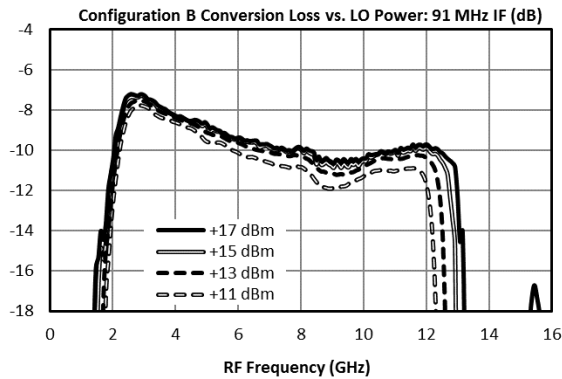
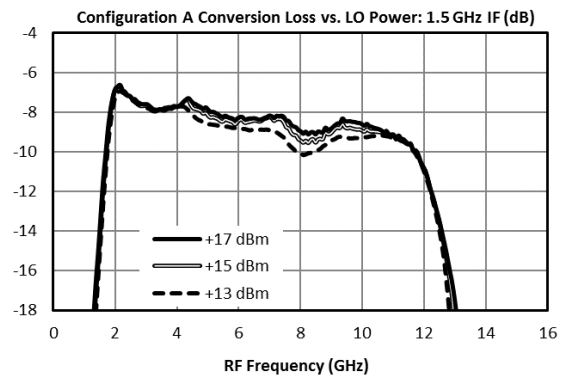
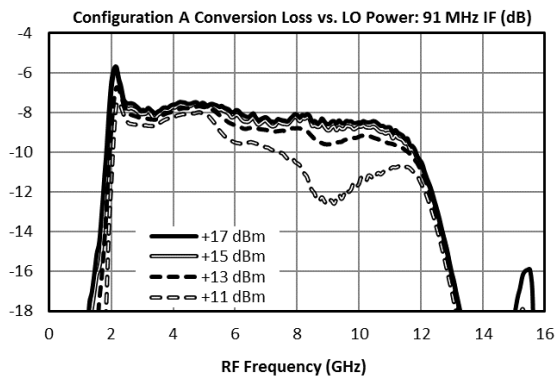
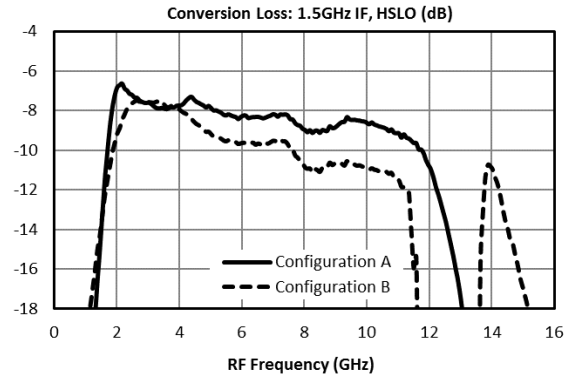
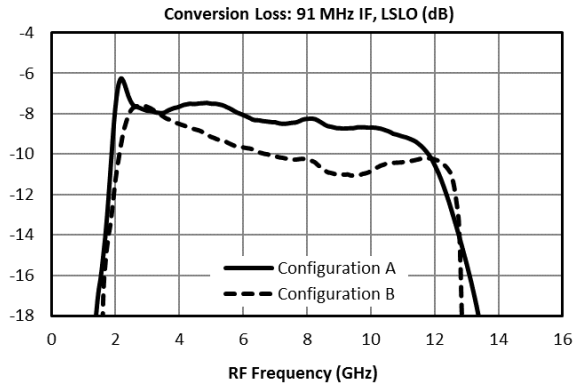
The electrical specifications apply at TA=+25°C in a 50Ω system. Typical data shown is for a down conversion application with a +15dBm sine wave LO input. Specifications shown for configuration A (B).

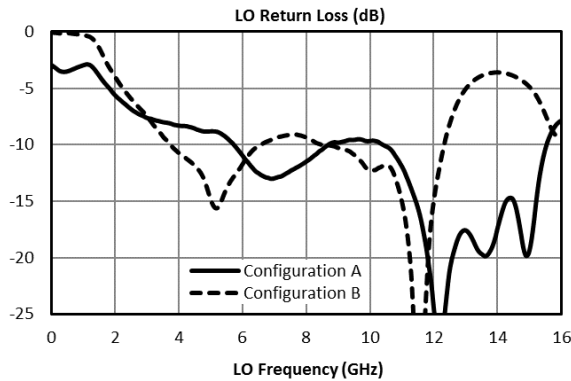
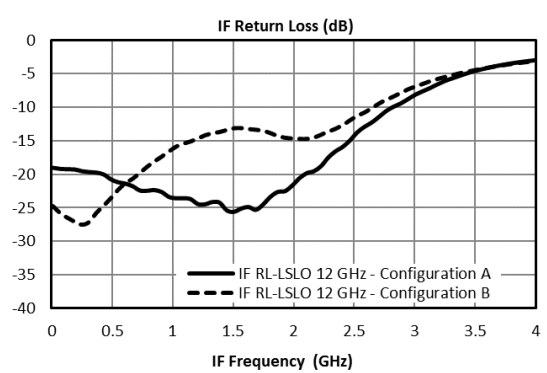
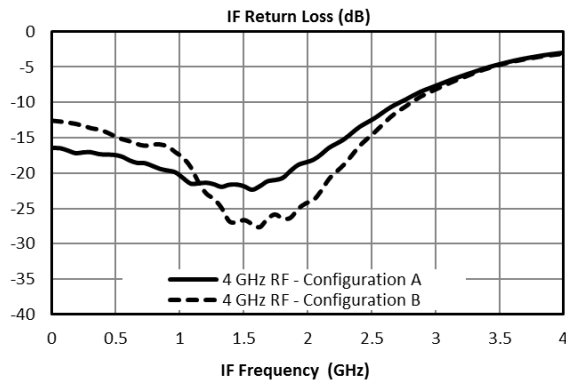
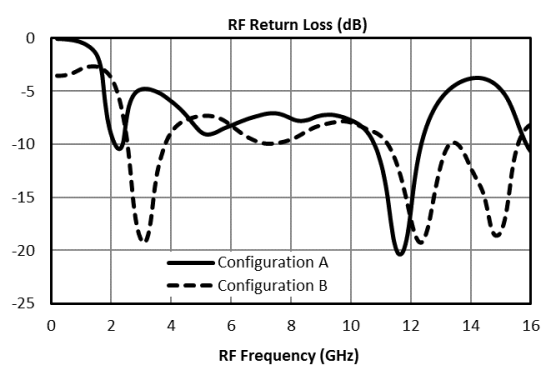
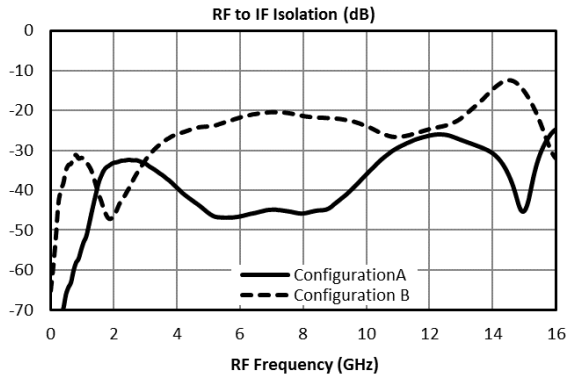
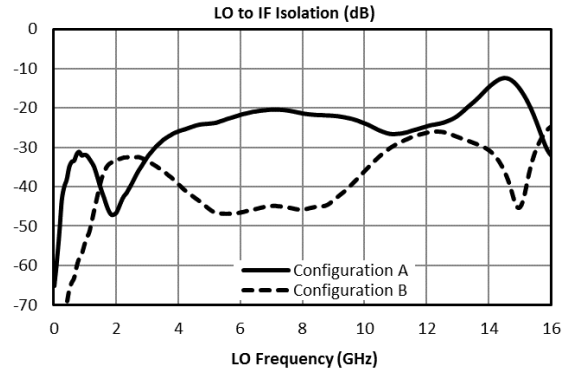
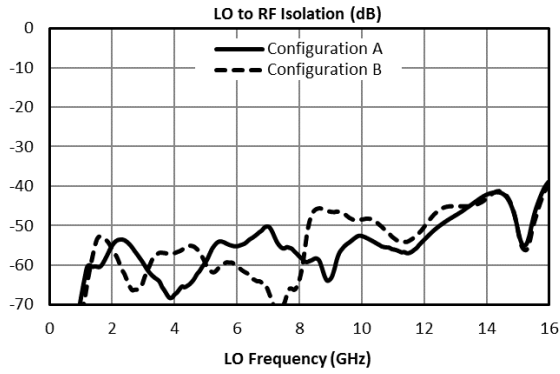
Parameter		Test Conditions	Min	Typical	Max	Units
RF (Pin 16) Frequency Range			2		12	GHz
LO (Pin 3) Frequency Range			2		12	
I (Pin 9) Frequency Range			0		3	
Conversion Loss (CL) ²		RF/LO = 2 - 12 GHz I = DC - 0.2 GHz		8.5 (10)	11.5 (13)	dB
		RF/LO = 2 - 12 GHz I = 0.2 - 3 GHz		9 (11.5)		
Noise Figure (NF) ³		RF/LO = 2 - 12 GHz I = DC - 0.2 GHz		9		dB
Isolation	LO to RF	RF/LO = 2 - 12 GHz		57		dB
	LO to IF	IF/LO = 2 - 12 GHz		26		
	RF to IF	RF/IF = 2 - 12 GHz		40		
Input IP3 (IIP3)		RF/LO = 2 - 12 GHz I = DC - 0.2 GHz		+25 (+21)		dBm
Input 1 dB Gain Compression Point (P1dB)				+12 (+14)		dBm

² Measured as a down converter to a fixed 91MHz IF.

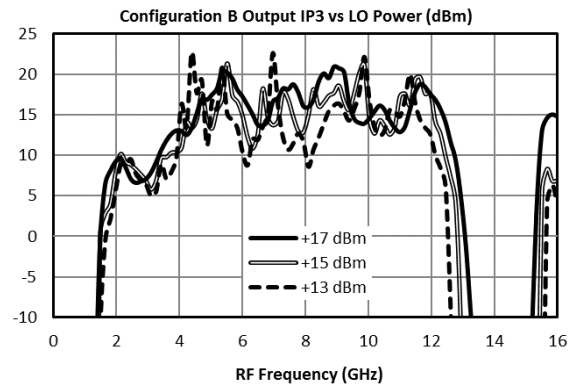
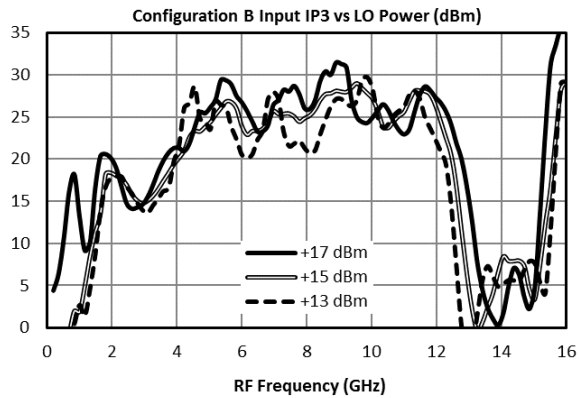
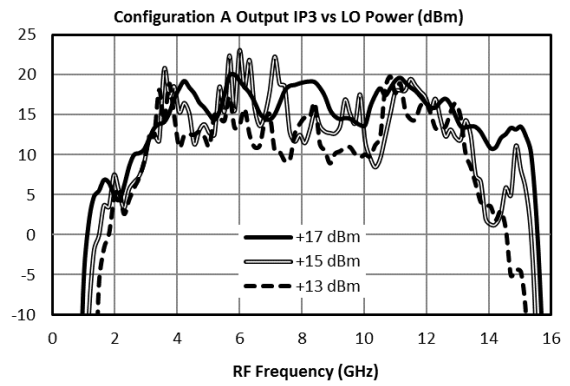
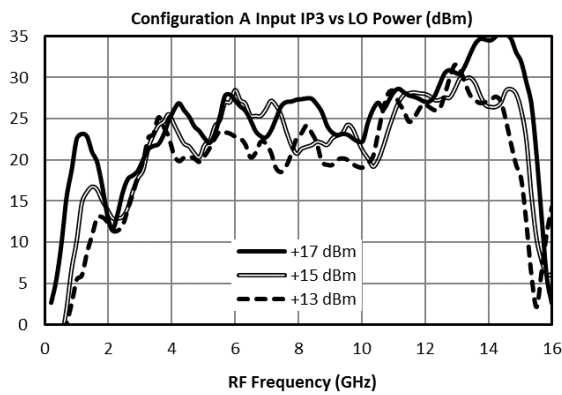
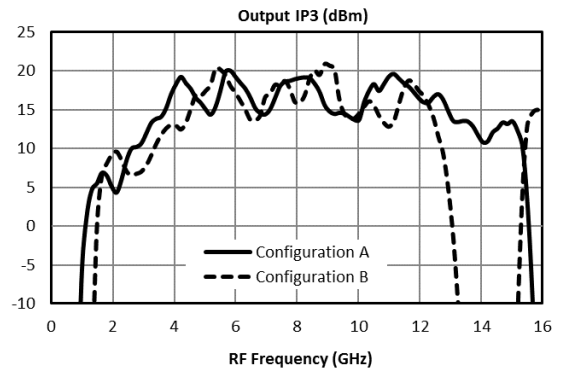
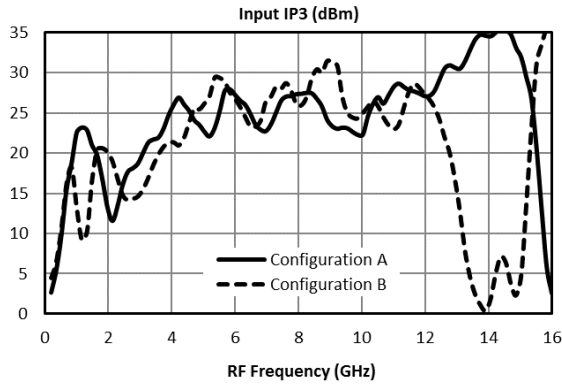
³ Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.

3.6 Typical Performance Plots

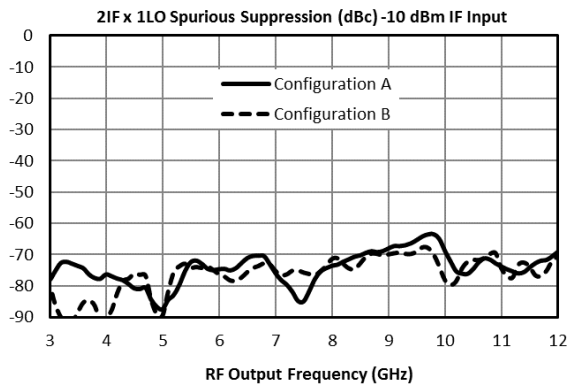
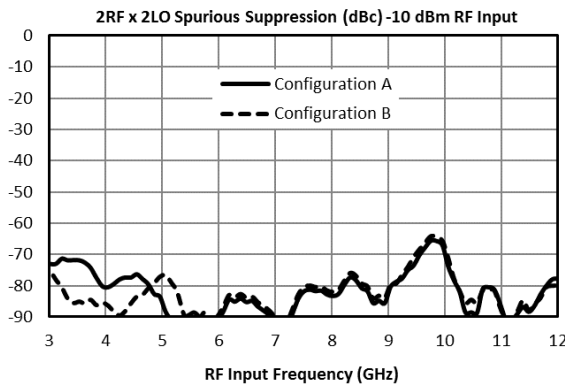
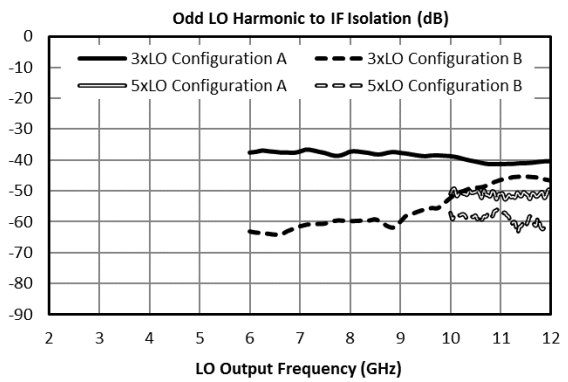
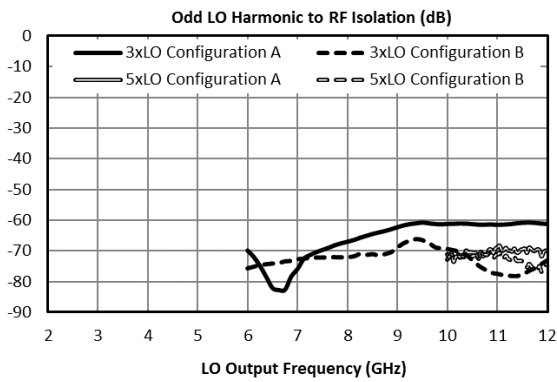
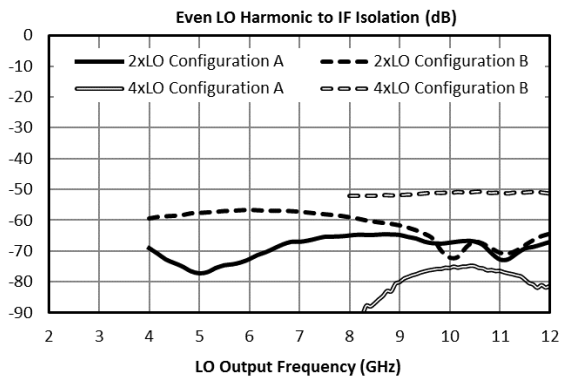
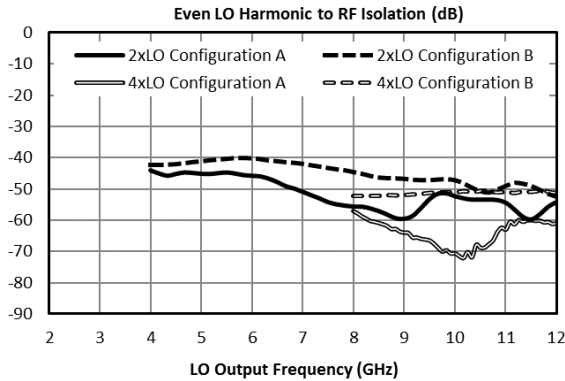




3.6.1 Typical Performance Plots: IP3



3.6.2 Typical Performance Plots: LO Harmonic Isolation



3.6.3 Typical Spurious Performance: Down-Conversion

Typical spurious data is provided by selecting RF and LO frequencies ($\pm m \cdot \text{LO} \pm n \cdot \text{RF}$) within the RF/LO bands, to create a spurious output within the IF band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by $(n-1)$, where “n” is the RF spur order. For example, the $2\text{RF} \times 2\text{LO}$ spur is 81 dBc for a -10 dBm input, so a -20 dBm RF input creates a spur that is $(2-1) \times (-10 \text{ dB})$ lower, or 91 dBc. Data is shown for the frequency plan in 3.6 Typical Performance. $m\text{LO} \times n\text{RF}$ plots can be found in section 3.6.2 Typical Performance Plots: LO Harmonic Isolation. $0\text{LO} \times 1\text{RF}$ plot is identical to the plot of LO-RF isolation.

Typical Down-conversion spurious suppression (dBc): Config A (B)

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
0xRF	-	58 (59)	57 (46)	67 (74)	63 (53)	71 (73)
1xRF	30 (15)	Reference	34 (33)	11 (13)	43 (41)	25 (26)
2xRF	78 (77)	58 (57)	81 (81)	69 (68)	73 (74)	70 (67)
3xRF	114 (107)	61 (63)	91 (93)	78 (79)	90 (94)	69 (72)
4xRF	149 (147)	106 (107)	117 (119)	115 (117)	124 (130)	116 (119)
5xRF	159 (158)	118 (120)	127 (133)	118 (122)	136 (143)	122 (128)

3.6.4 Typical Spurious Performance: Up-Conversion

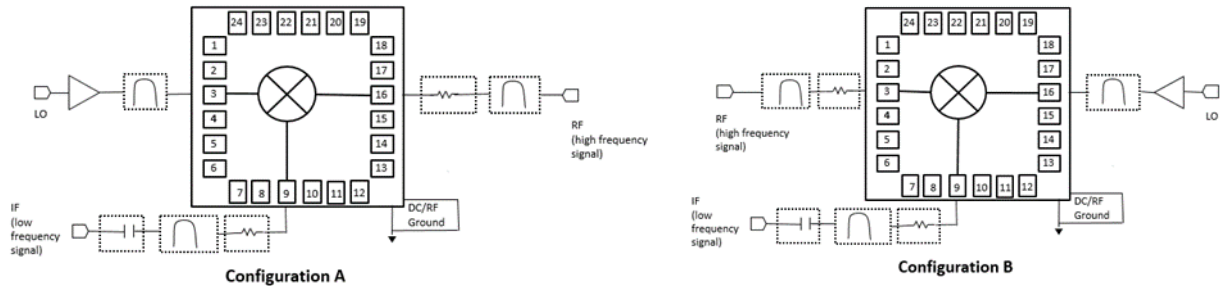
Typical spurious data is taken by mixing an input within the IF band, with LO frequencies ($\pm m \cdot \text{LO} \pm n \cdot \text{IF}$), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by $(n-1)$, where “n” is the IF spur order. For example, the $2\text{IF} \times 1\text{LO}$ spur is typically 73 dBc for a -10 dBm input with a sine-wave LO, so a -20 dBm IF input creates a spur that is $(2-1) \times (-10 \text{ dB})$ lower, or 83 dBc. Data is shown for the frequency plan in 3.6 Typical Performance.

Typical Up-conversion spurious suppression (dBc): Config A (B)

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
0xIF	-	88 (87)	70 (63)	43 (60)	78 (73)	54 (71)
1xIF	28 (19)	Reference	35 (34)	11 (12)	43 (39)	32 (27)
2xIF	55 (75)	73 (72)	59 (58)	77 (81)	66 (56)	73 (74)
3xIF	100 (83)	73 (71)	84 (84)	64 (66)	87 (88)	72 (69)
4xIF	118 (119)	124 (126)	114 (113)	119 (126)	111 (105)	117 (119)
5xIF	143 (138)	114 (115)	133 (132)	105 (108)	129 (125)	112 (105)

4. Operation

4.1 Application Circuit



4.2 Ports Operation

IF Port – Used as input on an upconversion, output on downconversion, or LO port in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads. Blocking capacitor is recommended if DC voltage is present on the line.

RF Port – Used as input on a downconversion, output on upconversion, or output in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads.

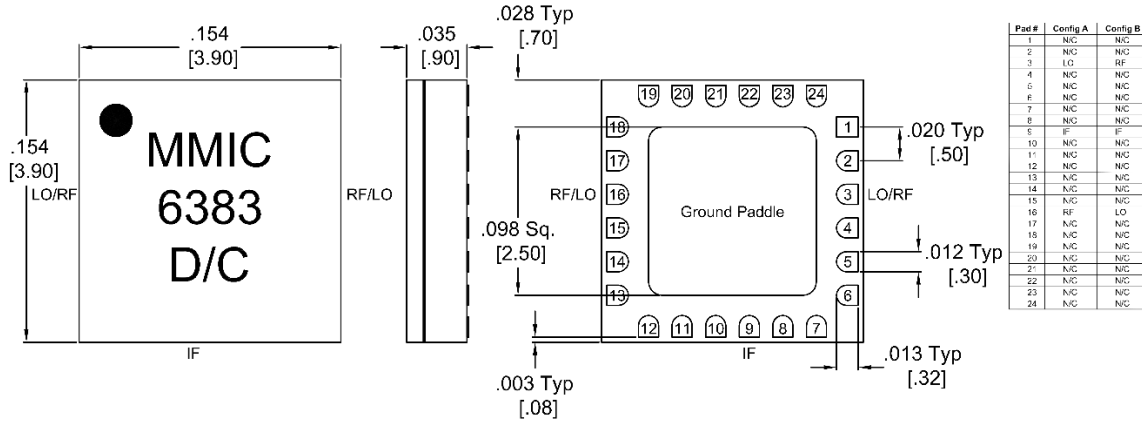
Filtering and Matching- Filtering is generally desired for spurious and image removal on the output port of the mixer. Reflective filters can cause out of band signals to reflect back into the mixer and cause conversion loss ripple, erroneous spurs, and other undesired behaviors. To eliminate these problems it is recommended that the filters be placed as close to the output port as possible. If undesired behavior is still observed, a diplexer with one port terminated or a 1-3 dB attenuator may reduce this problem.

RF Ground – The ground paddle of the QFN should be connected to a low noise RF ground with very low electrical resistance for high frequency operation.

LO Port – The noise floor of the LO input signal should be less than the value of the noise floor plus isolation of the mixer, or a filter is recommended to prevent reduction in dynamic range. An LO amplifier is required if the LO power is below the recommended drive level. It is important to use an amplifier with a broadband 50 ohm match such that it does not reflect spurious signals back into the mixer or other system circuitry.

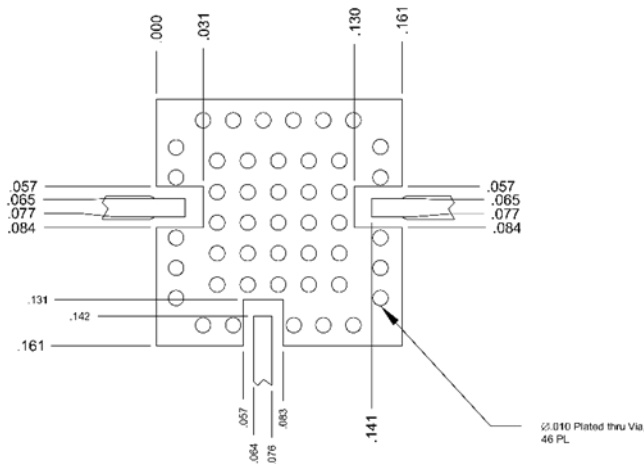
5. Mechanical Data

5.1 SM Package Outline Drawing



- Substrate material is ceramic.
- I/O Leads and Ground Paddle plating is (from base to finish):
 - Ni: 8.89um MAX 1.27um MIN
 - Pd: 0.17um MAX 0.07um MIN
 - Au 0.254um MAX 0.03um MIN
- All unconnected pads should be connected to PCB RF ground.

5.2 SM Package Footprint

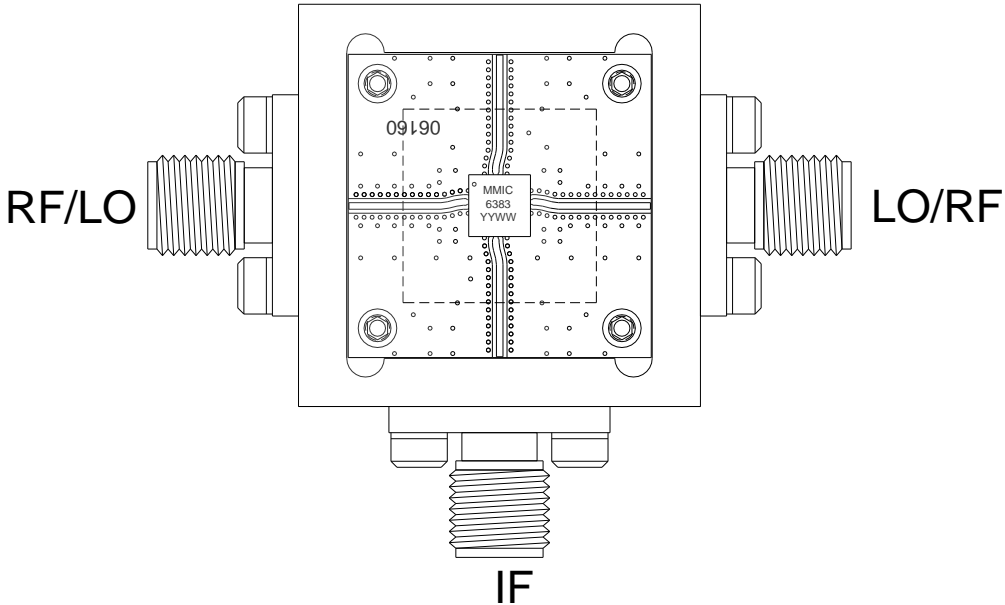


QFN-Package Surface-Mount Landing Pattern

[Click here for a DXF of the above layout.](#)

[Click here for leaded solder reflow.](#) [Click here for lead-free solder reflow.](#)

5.3 Evaluation Board Outline Drawing



Configuration A	Configuration B	Connector Type
RF	LO	SMA Female
LO	RF	SMA Female
IF	IF	SMA Female
Note: Eval connectors are not removeable		

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