

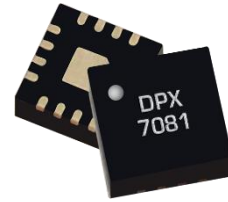
## Passive MMIC Diplexer/Reflectionless Filter

## MDPX-0407PSM

### 1 Device Overview

#### 1.1 General Description

The MDPX-0407PSM is a MMIC surface mount diplexer capable of multiplexing low frequency DC to 4 GHz and high frequency 7 to 26.5 GHz signals. Passive GaAs MMIC technology allows production of smaller filter constructions that replace larger form factor circuit board constructions. Tight fabrication tolerances allow for less unit-to-unit variation than traditional filter technologies. The MDPX-0407PSM is available as a 3x3mm QFN. Low unit to unit variation allows for accurate simulations using the provided S3P file taken from measured production units.



PSM

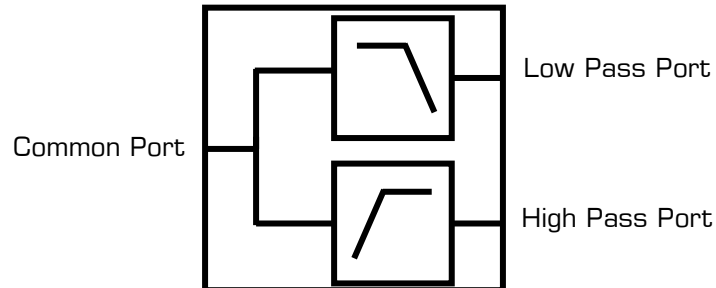
#### 1.2 Features

- Excellent Return Loss
- 5.5 GHz Crossover Point
- High Stop Band Suppression
- Reflectionless Filter
- [S3P](#) data available

#### 1.3 Applications

- Satellite Communications
- Reflectionless Filter Applications
- Electronic Warfare

#### 1.4 Functional Block Diagram



#### 1.5 Part Ordering Options<sup>1</sup>

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
MDPX-0407PSM-2	3 x 3 mm QFN	PSM	RoHS	Active	EAR99
EVB-MDPX-0407P	Connectorized Evaluation Fixture	EVB	RoHS	Active	EAR99

<sup>1</sup> Refer to our [website](#) for a list of definitions for terminology presented in this table.

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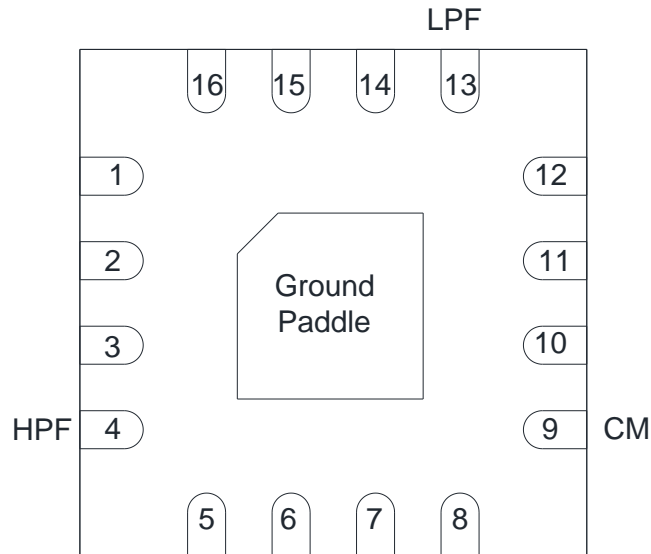
## Revision History

Revision Code	Revision Date	Comment
-	January 2023	Datasheet Initial Release

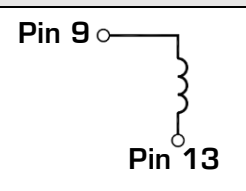
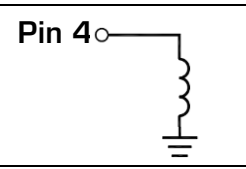
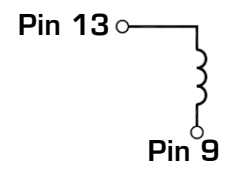
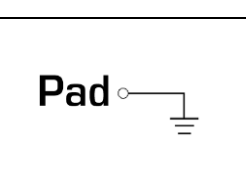
## 2 Port Configurations and Functions

### 2.1 Port Diagram

A top-down x-ray view of the MDPX-0407PSM package outline drawing is shown below. Input to the diplexer is on Pin 9, Pin 4 will be the output after passing through the HPF and Pin 13 will be the output after passing through the LPF.



### 2.2 Port Functions

Port	Function	Description	Equivalent Circuit
Pin 9	Common/Input	Pin 9 is DC short to Pin 13 and open to GND and Pin 4.	
Pin 4	High Pass Filter	Pin 4 is DC short to GND and open to the other ports.	
Pin 13	Low Pass Filter	Pin 13 is DC short to Pin 9 and open to GND and Pin 4.	
Pad	Ground	PSM package ground path is provided through the substrate and ground bond pads.	

## 3 Specifications

### 3.1 Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. All Absolute Maximum Ratings are individual and should not be met in parallel. If these limits are exceeded, the device may be inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Units
Low Pass Port DC Current	N/A	mA
Common Port DC Current	N/A	mA
RF Power Handling	+30	dBm
Operating Temperature	-65 to +100	°C
Storage Temperature	-65 to +100	°C

### 3.2 Package Information

Parameter	Details	Rating
ESD	Human Body Model (HBM), per MIL-STD-750, Method 1020	N/A

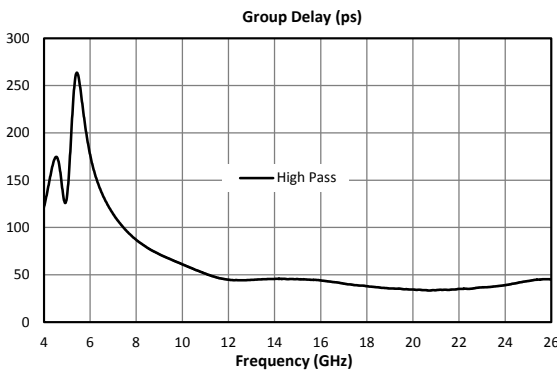
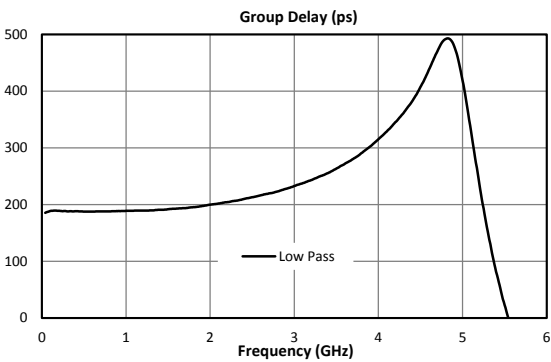
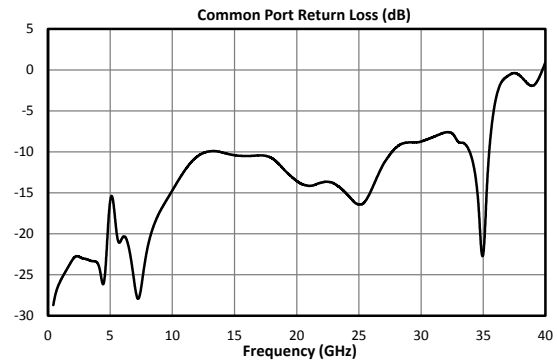
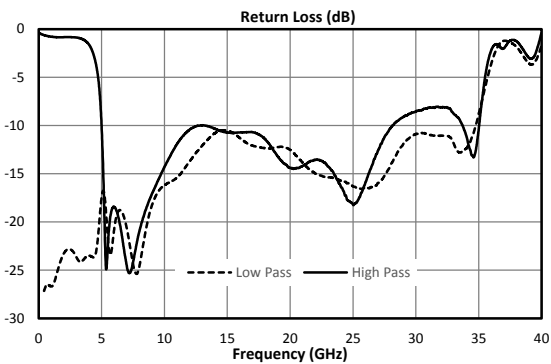
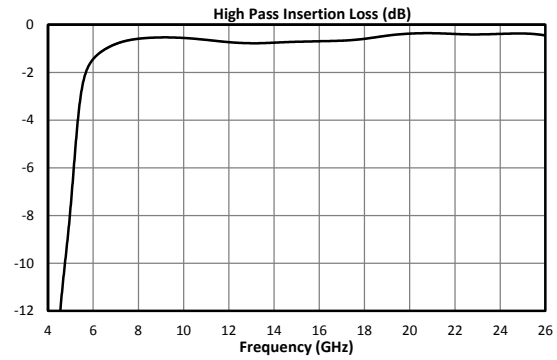
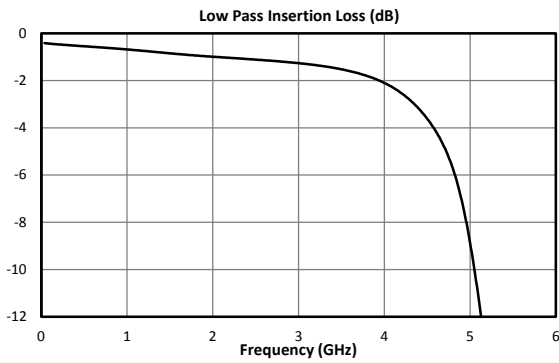
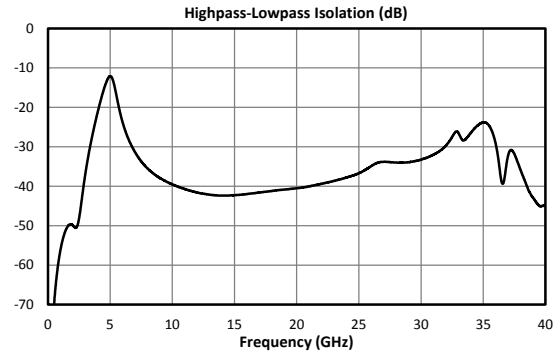
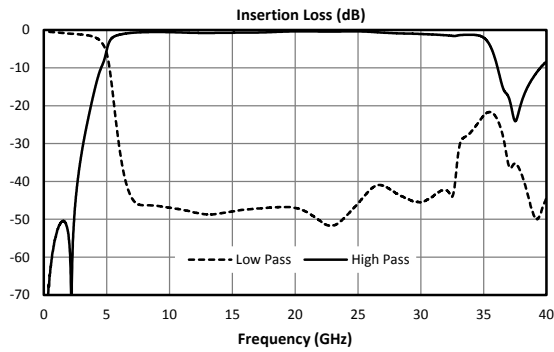
### 3.3 Electrical Specifications

The electrical specifications apply at  $T_A=+25^\circ\text{C}$  in a  $50\Omega$  system. Typical data shown is for the filter in a PSM package with a sine wave input applied to Pin 9.

Min and Max limits are guaranteed at  $T_A=+25^\circ\text{C}$ .

Parameter	Frequency Range (GHz)	Min	Typ	Max
<b>Low Pass Filter</b>				
Pass Band Insertion Loss (dB)	DC to 4		0.9	
Stop Band Rejection (dB)	7 to 26.5	25	42	
Pass Band Return Loss (dB)	DC to 4	22	24	
<b>High Pass Filter</b>				
Pass Band Insertion Loss (dB)	7 to 26.5		0.9	
Stop Band Rejection (dB)	DC to 3	25	53	
Pass Band Return Loss (dB)	7 to 26.5	10	14	
Common Port Return Loss (dB)	DC to 4	19	23	
	7 to 26.5	10	14	
Isolation (dB)	DC to 3	25	38	
	3 to 4		20	
	7 to 26.5	25	38	
Impedance ( $\Omega$ )			50	

### 3.4 Typical Performance Plots<sup>2</sup>

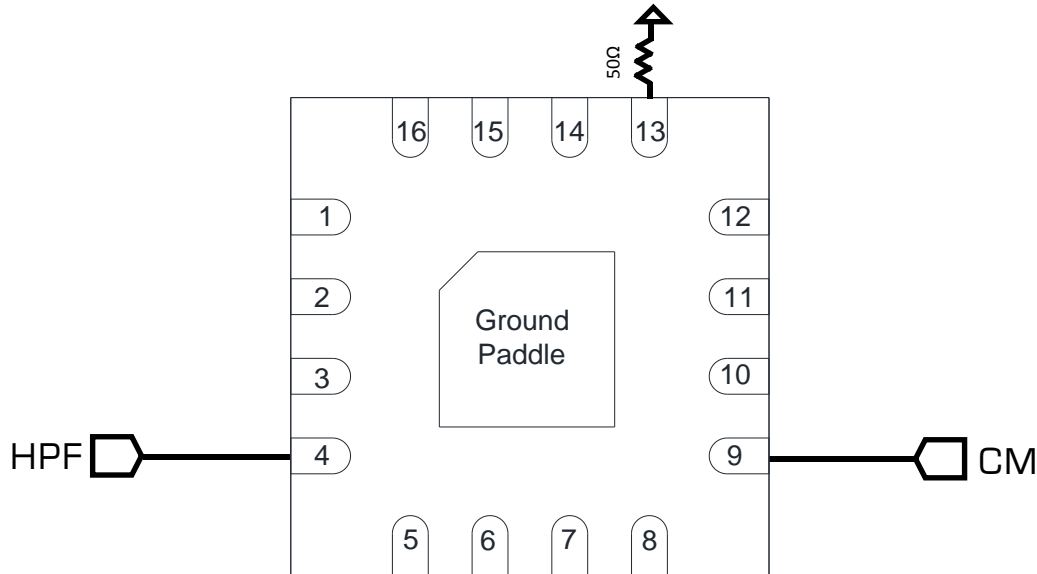


<sup>2</sup> Typical performance plots are evaluation board measurements with fixturing to the device pads de-embedded.

## 4 Application

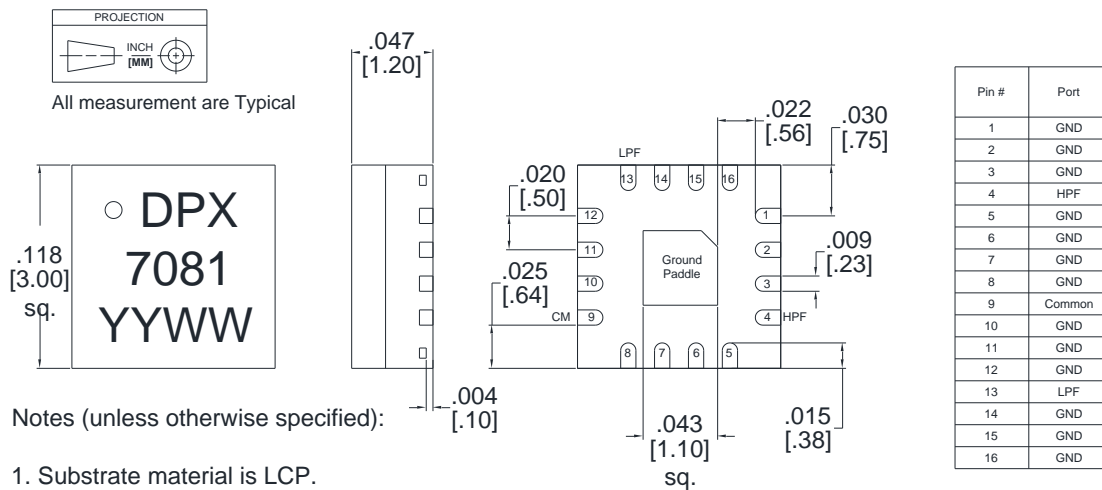
### 4.1 Example Reflectionless Filter

A top-down view of the MDPX-0407PSM's PSM package outline drawing is shown below as a high pass reflectionless filter. A low pass reflectionless filter can be achieved by terminating Pin 4 and taking the output at Pin 13 instead.



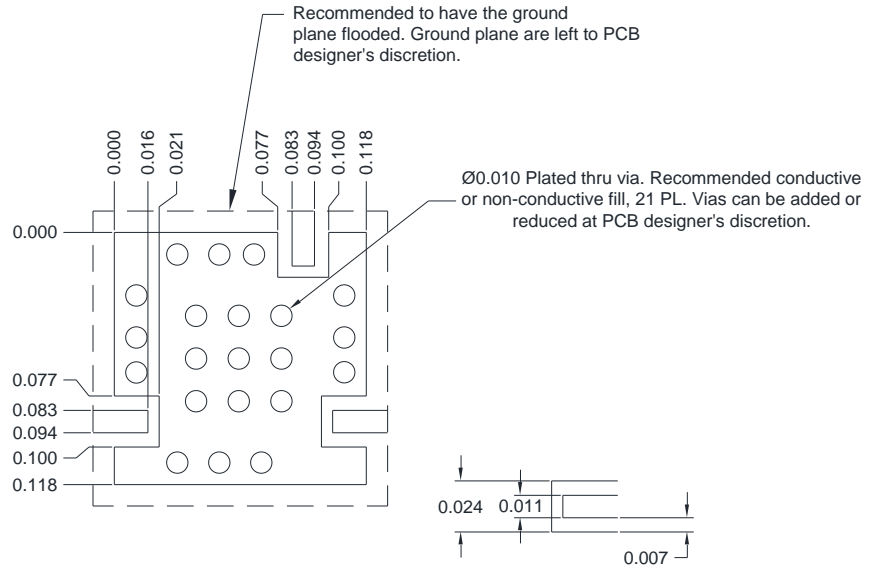
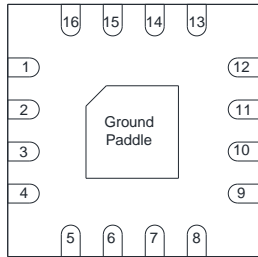
## 5 Mechanical Data

### 5.1 PSM Package Outline Drawing



### 5.2 PSM Package Footprint

QFN 3mm Sample Drawing  
X-Ray view



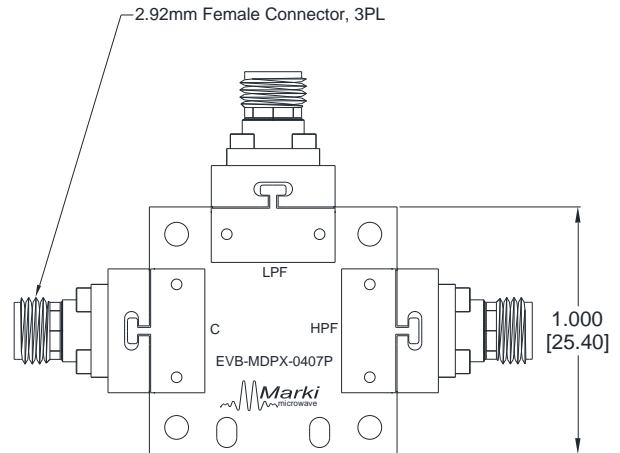
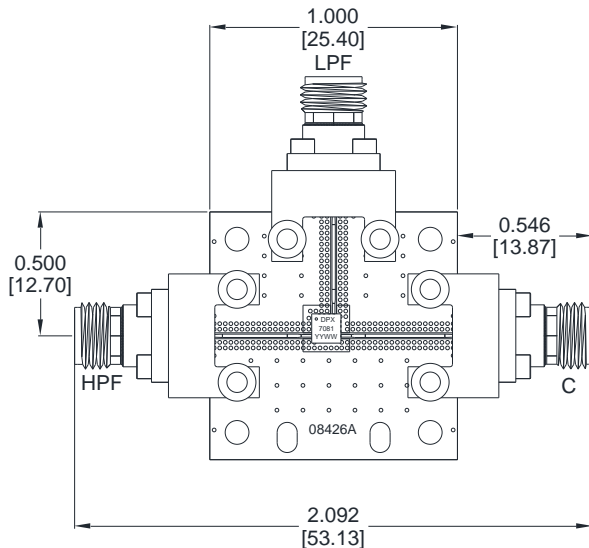
Material Rogers 4003 008"  $\frac{1}{2}$  Oz Cu.

[QFN-Package Surface-Mount Landing Pattern](#)

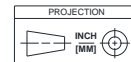
[Click here for a DXF of the above layouts.](#)

[Click here for leaded solder reflow.](#) [Click here for lead-free solder reflow](#)

### 5.3 EVB Package Outline



All measurements are typical



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