

Typical Performance

Insertion Loss data was taken using TRL de-embedding procedures.

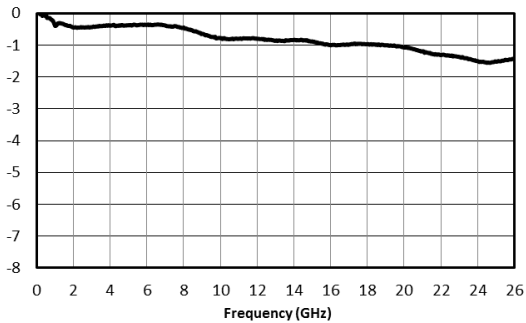


Fig. 1. RF insertion loss.

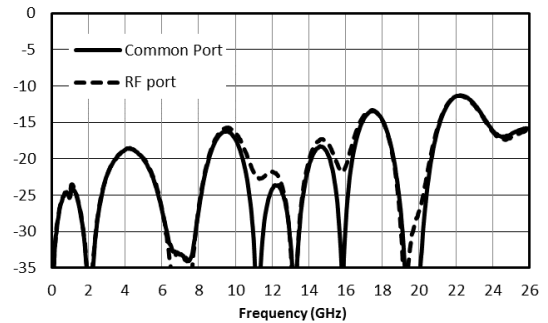


Fig. 2. Return loss

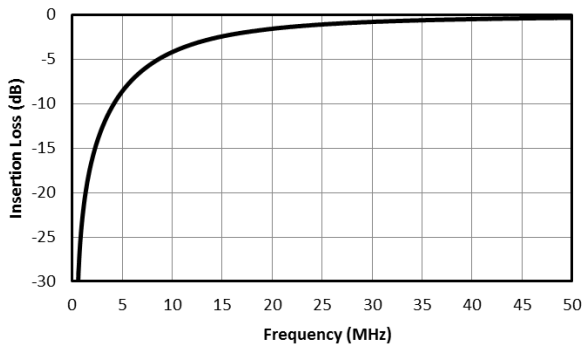


Fig. 3. Low frequency RF response

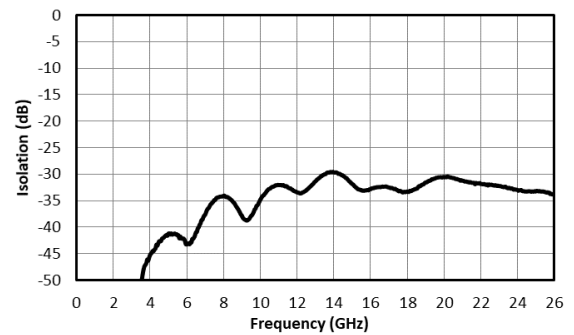


Fig. 4. DC- RF Isolation

MINIATURE SURFACE MOUNT BIAS TEE

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Page 3

Typical Performance vs Bias Current at Low frequencies

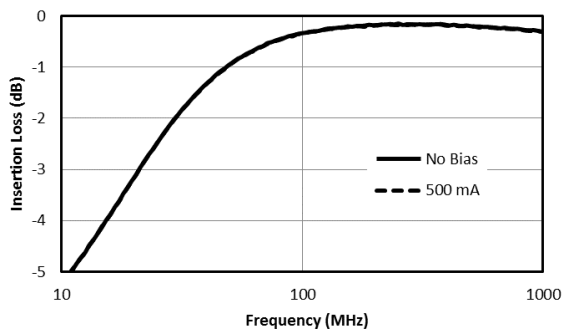


Fig. 4. Insertion Loss vs Bias Current.

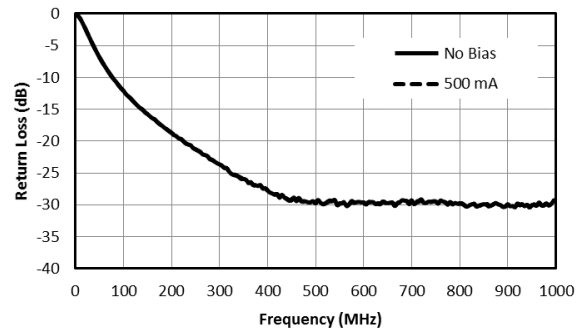


Fig. 5. Common Return Loss vs Bias Current.

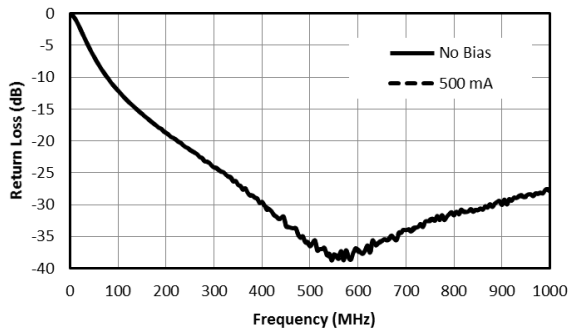


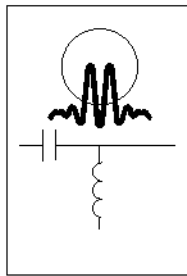
Fig.6. RF Return Loss vs Bias Current.

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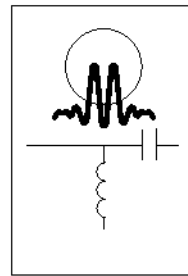
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Page 4

Port Configuration Option



BT-0026SMG-1



BT-0026SMG-2

Revision History

Revision code	Revision Date	Comment
A	4/2021	DC-RF Isolation Plot added

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