

LEAD-FREE / RoHS-COMPLIANT

SURFACE-MOUNT BROADBAND BALUN

BAL-0003SMG

Features

- 500 kHz to 3 GHz 1:2 Balun (Balanced to Unbalanced Transformer)
- Transforms 50 Ω Input to 100 Ω Differential (50 Ohm Single) Output
- Tuned for Optimal Phase/Amplitude Balance
- Applications: Analog to Digital Converters, Balanced Receivers, Baseband Digital Modulation, Signal Integrity
- [BAL-0003SMG.s3p](#)



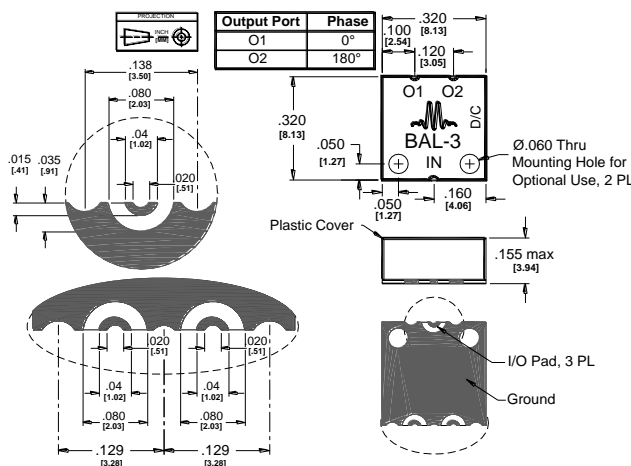
Electrical Specifications - Specifications guaranteed from -55 to +100°C, measured in a 50 Ω system.

Parameter	Frequency Range	Min	Typ	Max	
Nominal Insertion Loss (dB)	500 kHz to 3 GHz		6		
Nominal Phase Shift (Degrees)			180		
Amplitude Balance (dB)			± 0.3	± 0.8	
Phase Balance (Degrees)			± 3	± 8	
Common Mode Rejection (dB)		25	35		
Excess Insertion Loss (dB) ¹			0.8	2	
Isolation (dB)			9		
VSWR			1.5		
Input Power (W)					1
Risetime /Falltime (ps) ²				17	

¹Excess Insertion Loss = (Common Port to Output Port Insertion Loss) – 6 dB.

²Specified as 90%/10%. Calculated from $\tau_{\text{balun}}^2 = (\tau_{\text{out}}^2 - \tau_{\text{in}}^2)$ with a 6 Gb/s input pattern.

Model Number	Description
BAL-0003SMG	500 kHz to 3 GHz Balun, Surface Mount, LEAD-FREE/RoHS COMPLIANT



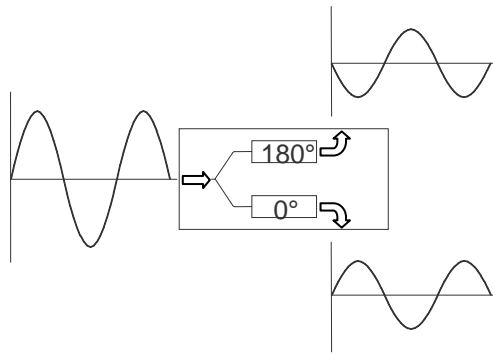
Substrate material is 8-mil thick Rogers 4003, 1 Oz Electrodeposited Cu. I/O Pads & Ground Plane Finish is Gold Flash, 5 to 10 μ -inches, over Electroplated Nickel, 100-200 μ -inches, over Cu. See [BALSMG-PCB](#) for suggested PCB layout.

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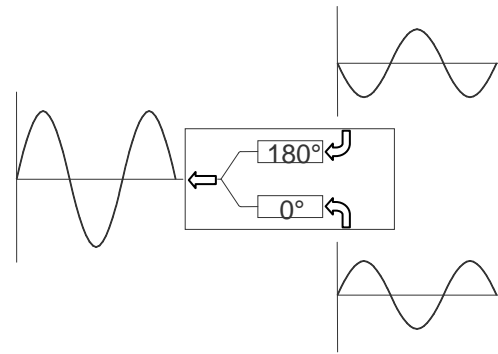
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Block Diagram



Single ended to differential



Differential to single ended

Typical Performance

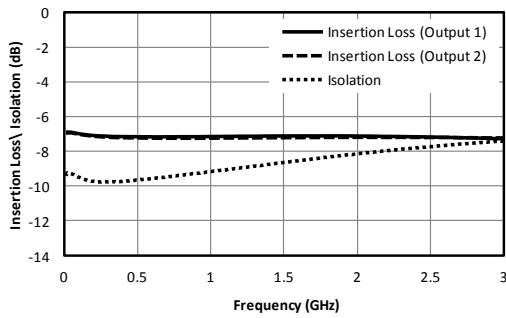


Fig. 1. Common to output port insertion loss and output to output port Isolation.

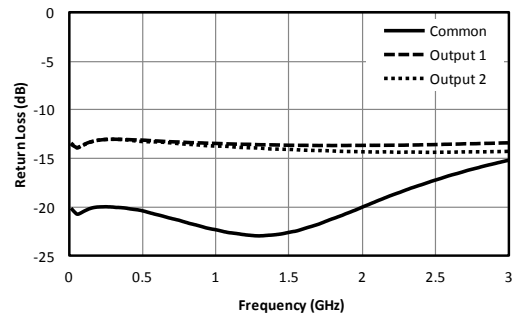


Fig. 2. Return loss for common port and output ports.

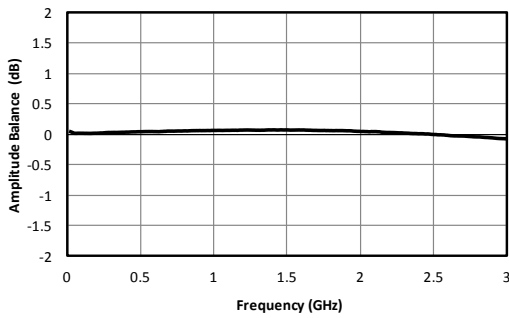


Fig. 3. Amplitude balance between output ports.

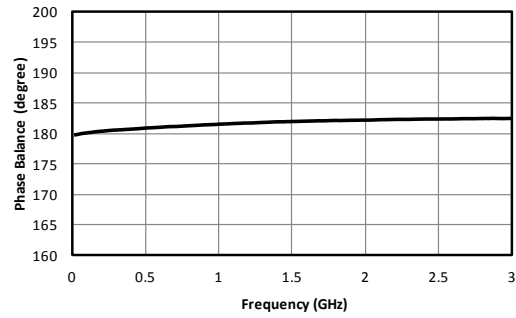


Fig. 4. Phase balance between output ports.

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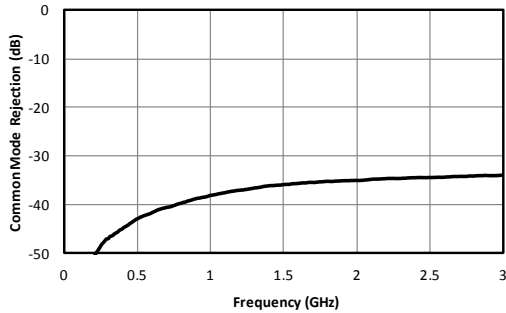


Fig. 5. Common mode rejection.

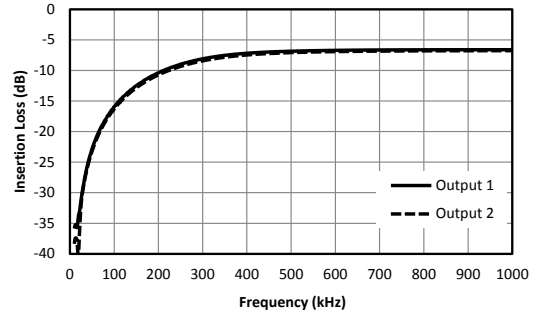


Fig. 6. Low Frequency Response

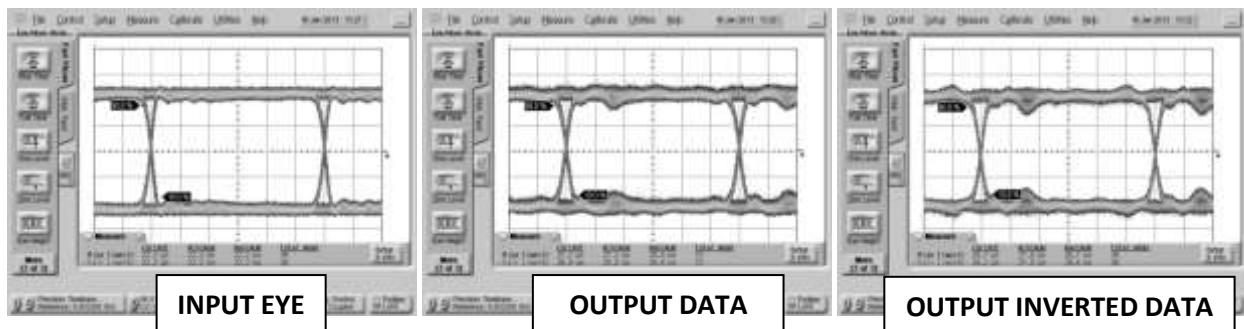
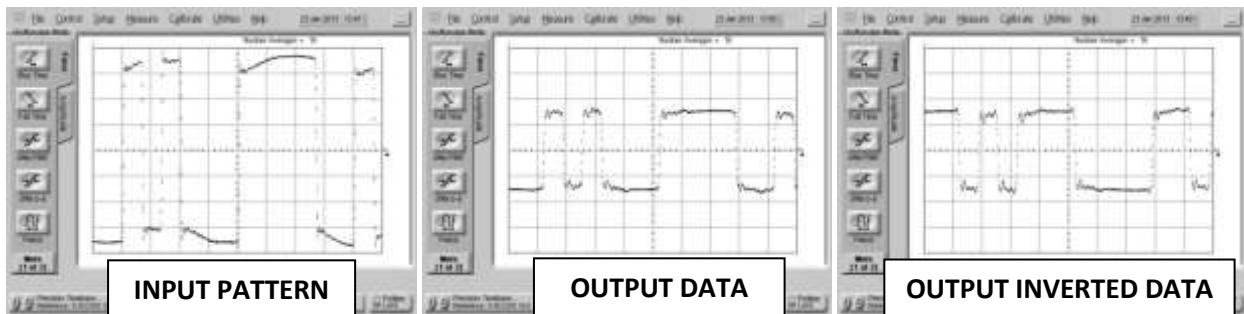


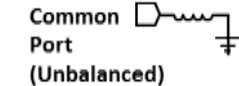
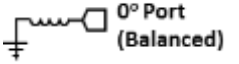
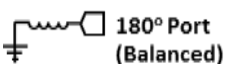
Fig. 6. Oscilloscope measurements of the BAL-0003SMG with a 3 Gb/s PRBS pattern. Bit pattern is measured with a 2^7-1 PRBS input demonstrating extremely good pulse fidelity for both inverted and non-inverted output. Eye diagrams are taken with a $2^{31}-1$ PRBS input demonstrating minimal eye distortion/closure afforded by the extremely low frequency operation of the balun (<500 kHz).

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DC Interface

Port	Description	DC Interface Schematic
Common Port / In (Unbalanced)	The common port is DC short to ground.	 <p>Common Port (Unbalanced)</p>
Out 1 / 0° Port (Balanced)	The 0° port is DC short to ground.	 <p>0° Port (Balanced)</p>
Out 2 / 180° Port (Balanced)	The 180° port is DC short to ground.	 <p>180° Port (Balanced)</p>

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