

DC – 14 GHz Cascadable Broadband InGaP MMIC Amplifier

AKA-1310

1. Device Overview

1.1 General Description

The AKA-1310 is a low-cost cascadable broadband InGaP HBT MMIC amplifier. This is a general-purpose gain block amplifier which provides excellent gain flatness below 6 GHz, high P1dB, high OIP3, and a very small die size. The simple application circuit requires minimal external components, allowing it to be used in a variety of applications. It is available in bare die form.



Bare Die

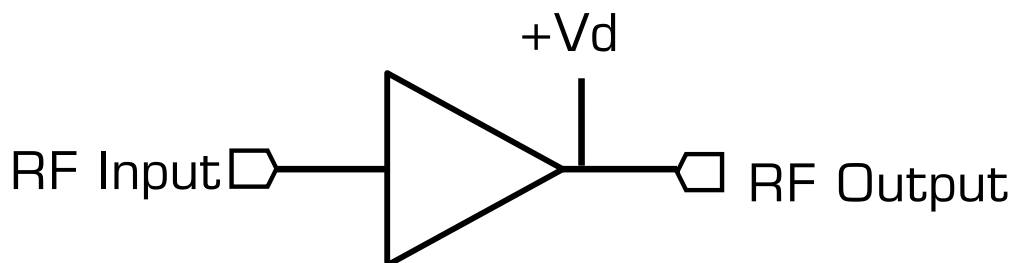
1.2 Features

- Small Die Size: 0.4 mm x 0.43 mm
- Excellent Gain Flatness
- High P1dB
- +28 dBm OIP3, 13 dB Gain at 2 GHz
- Single Power Supply Operation
- Low-Cost
- S-parameters: [AKA-1310D](#)

1.3 Applications

- Mobile test and measurement equipment
- Radar and satellite communications
- 5G Transceivers
- Driver Amplifier L-Diode Mixer

1.4 Functional Block Diagram



1.5 Part Ordering Options¹

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
AKA-1310D	Wire Bondable Die	Bare Die	RoHS	Active	EAR99

¹ Refer to our [website](#) for a list of definitions for terminology presented in this table.

Table of Contents

1. Device Overview	1	3.1 Absolute Maximum Ratings.....	4
1.1 General Description.....	1	3.2 Package Information	4
1.2 Features	1	3.3 Recommended Operating Conditions .	5
1.3 Applications	1	3.4 Sequencing Requirements	5
1.4 Functional Block Diagram	1	3.5 Electrical Specifications	6
1.5 Part Ordering Options.....	1	3.6 AKA-1310D Typical Performance Plots.....	7
2. AKA-1310 Port Configurations and Functions	3	4. Application Information	8
2.1 AKA-1310D Port Diagram	3	4.1 AKA-1310D Application Circuit.....	8
2.2 AKA-1310D Port Functions	3	5. Mechanical Data.....	9
3. Specifications	4	5.1 AKA-1310D Outline Drawing.....	9

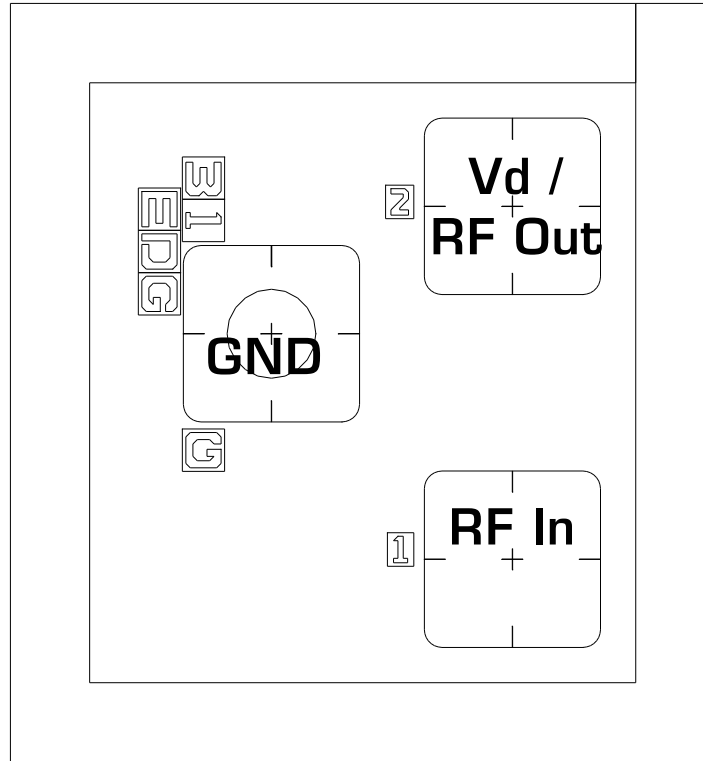
Revision History

Revision Code	Revision Date	Comment
-	May 2021	Datasheet Initial Release

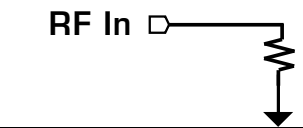
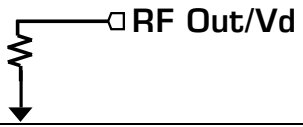

2. AKA-1310 Port Configurations and Functions

2.1 AKA-1310D Port Diagram

A port diagram of the AKA-1310D is shown below.



2.2 AKA-1310D Port Functions

Port	Function	Description	Equivalent Circuit for Package
RF In (1)	RF Input	This is the RF Input port of the amplifier die. It is RF matched to 50 Ω and requires an external DC blocking capacitor.	
Vd/RF Out (2)	RF Output and Positive Device Voltage Supply Port	This is the amplifier die's RF Output and positive supply voltage port, Vd. It is RF matched to 50 Ω and is DC coupled.	
GND (G)	Ground	Backside of the IC must be connected to a DC/RF ground with high thermal and electrical conductivity. Ground pad connected to IC backside with via.	

3. Specifications

3.1 Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may become inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Units
Positive Bias Current (I _{cc})	79	mA
Power Dissipation	366	mW
RF Input Power	+20	dBm
Operating Temperature	-40 to +85	°C
Storage Temperature	-65 to +150	°C
θ_{JC} , Junction to Case Thermal Resistance	179	°C/W
Max Junction Temperature for MTTF > 1E6 hours	150	°C

3.2 Package Information

Parameter	Details	Rating
ESD	Human Body Model (HBM), per MIL-STD-750, Method 1020	Class 1A

3.3 Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

	Min	Nominal	Max	Units
T _A , Ambient Temperature	-40	+25	+85	°C
Positive DC Device Voltage (V _d)	+4.5	+4.6	+4.7	V
Positive DC Current (I _{cc})	35	50	79	mA

3.4 Sequencing Requirements

There is no sequencing required to power up or power down the amplifier.

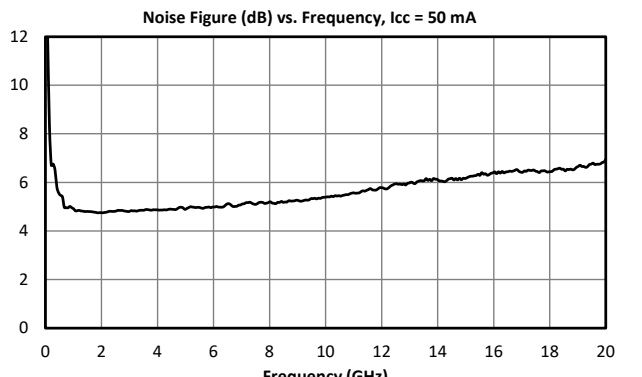
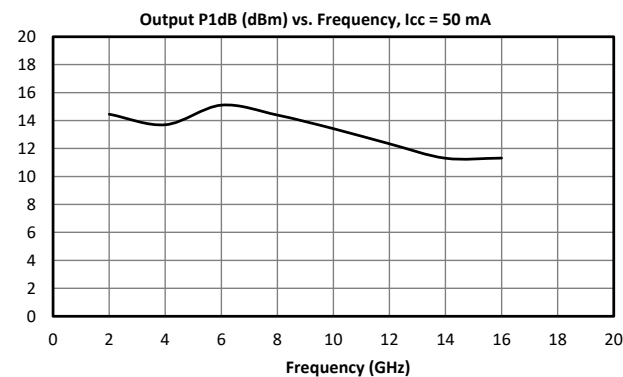
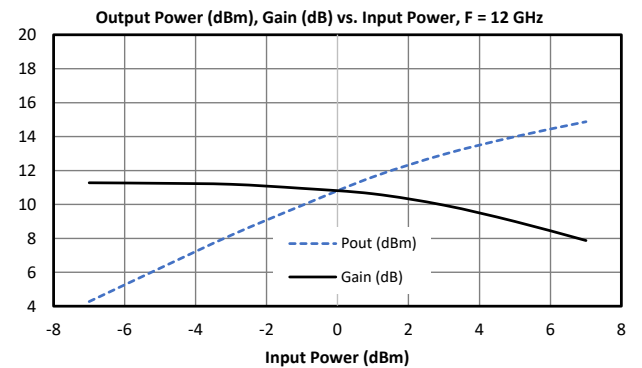
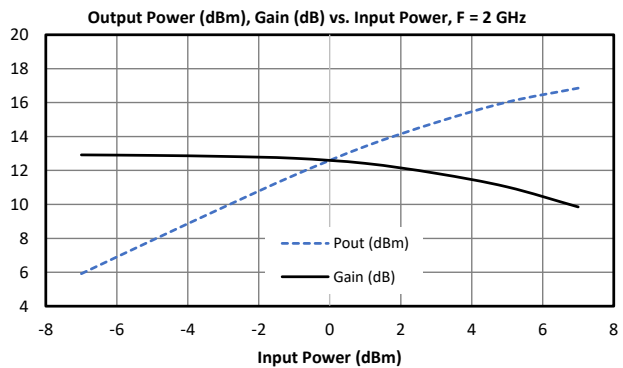
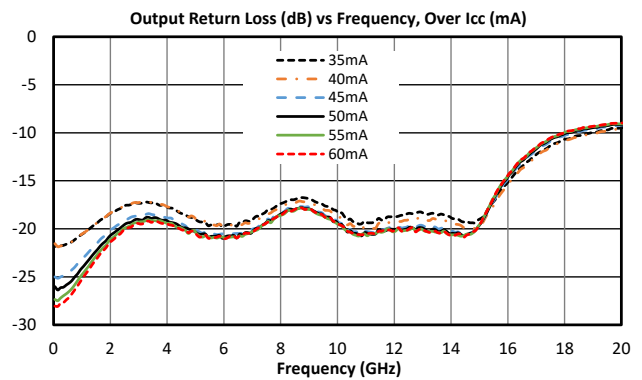
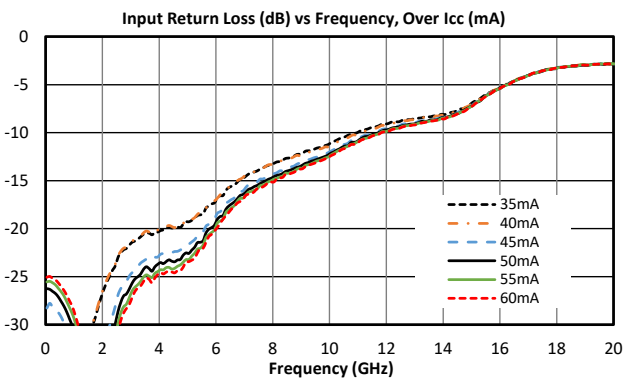
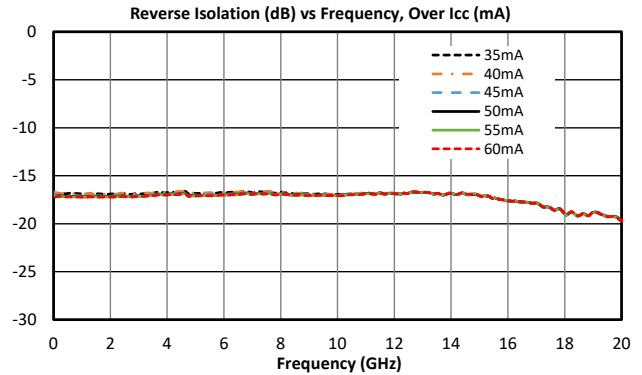
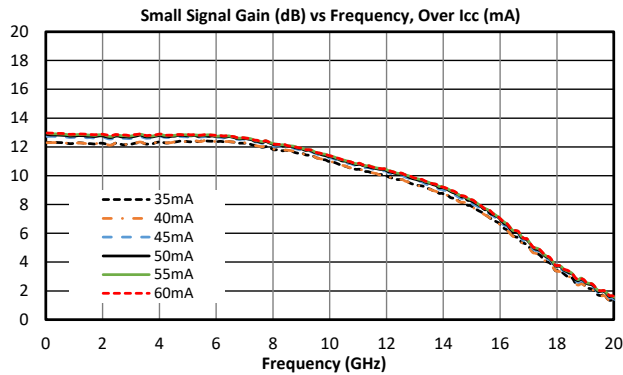
3.5 Electrical Specifications

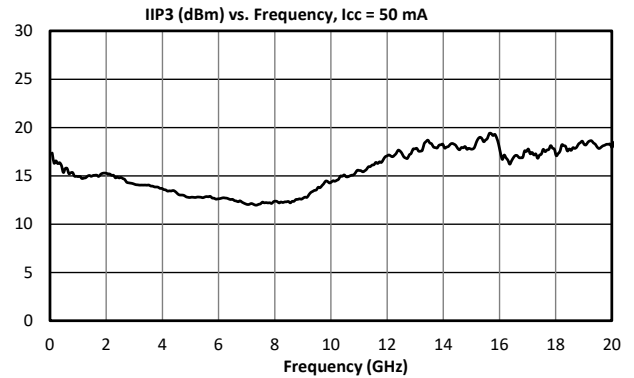
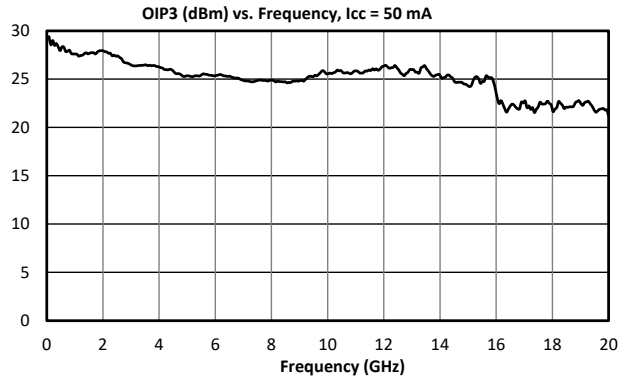
The electrical specifications apply at $T_A=+25^{\circ}\text{C}$ in a 50Ω system.

Die are 100% DC tested.

Parameter	Test Conditions	Frequency	Min	Typical	Units	
Small Signal Gain	Vd = +4.6 V, Icc = 50 mA	0.1 – 6.0 GHz	12	13	dB	
		6.0 – 14.0 GHz	9	11		
Output P1dB		2.0 - 6.0 GHz		14	dBm	
		6.0 – 14.0 GHz		13		
Input Return Loss		0.1 – 14.0 GHz			18	dB
Output Return Loss					20	
Reverse Isolation					17	
Noise Figure	3.0 GHz			5		
Output IP3 (OIP3)		2.0 GHz		+28	dBm	
Device Current, Icc	Vd = +4.6V			50	mA	

3.6 AKA-1310D Typical Performance Plots

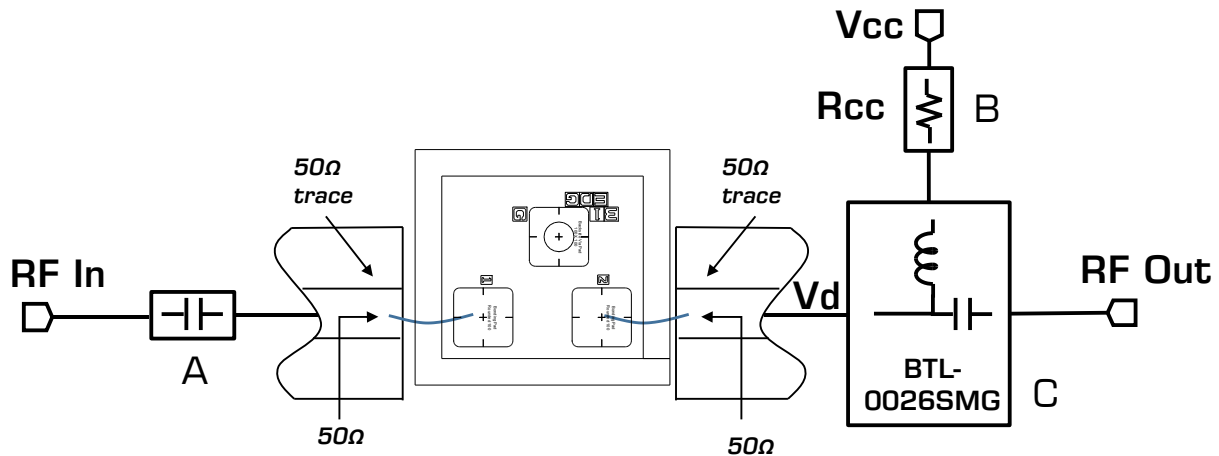




4. Application Information

4.1 AKA-1310D Application Circuit²

Below is the recommended application circuit for the AKA-1310D.



Designator	Description	Sample Part Number
A	0402 1.0 μ F SMT Capacitor	CL05A105KO5NNNC
B	0402 SMT Resistor	CPF0402B20RE1
C	Marki Surface-Mount Bias Tee; 500 kHz – 26 GHz	BTL-0026SMG

² Application circuit not drawn to scale. AKA-1310D chip is enlarged for viewing purposes.

The supply voltage, V_{cc} , is dropped to the device voltage, V_d , through the biasing resistor, R_{cc} . To calculate the appropriate value of this resistor, the designer simply uses the available power supply voltage and chosen bias current as follows.

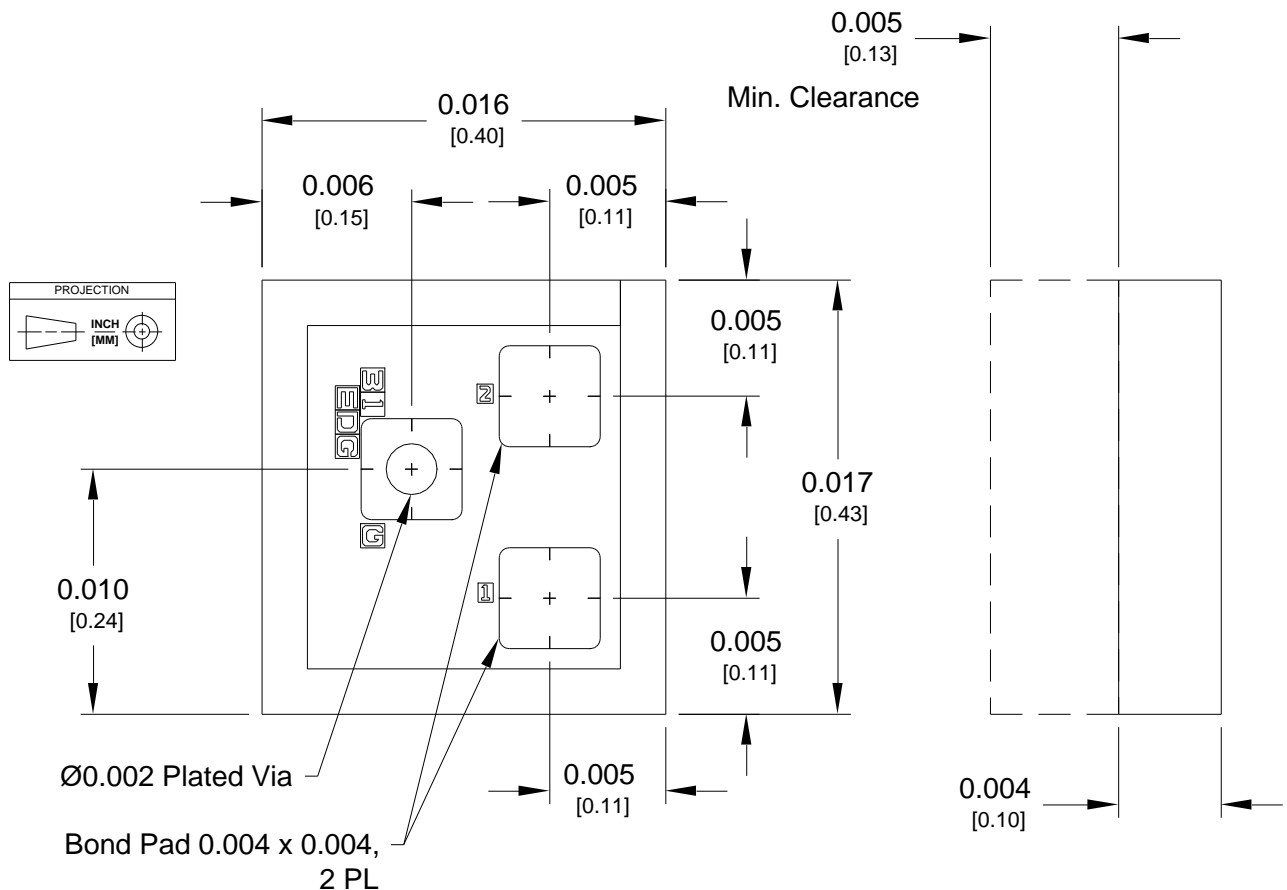
$$R_{cc} = \frac{V_{cc} - V_d}{I_{cc}}$$

Below is table of possible R_{cc} values.

Recommended Bias Resistor Values, $I_{cc} = 50 \text{ mA}$, $V_d = 4.6 \text{ V}$						
Power Supply Voltage, V_{cc} (V)	5	8	10	12	15	20
Bias Resistor, R_{cc} (Ω)	6	68	108	148	208	308

5. Mechanical Data

5.1 AKA-1310D Outline Drawing



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