1. Device Overview

1.1 General Description

The MM1-0832HSM is a GaAs MMIC double balanced mixer that is optimized for high frequency applications. MM1-0832HSM is a high frequency X band mixer that works well as both an up and down converter through Ka band. This mixer offers low conversion loss and high LO to RF isolations at moderate LO powers. The sister MM1-0832LSM is recommended for low power applications. The MM1-0832HSM is available in a 3x3 mm QFN package. Evaluation boards are available. For a list of recommended LO driver amps for all mixers and IQ mixers, see here.

1.2 Features

- High IF frequency
- RoHS Compliant

1.3 Applications

- Test and measurement equipment
- Electronic Warfare
- 5G
- SATCOM

1.4 Functional Block Diagram

1.5 Part Ordering Options

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Description</th>
<th>Package</th>
<th>Green Status</th>
<th>Product Lifecycle</th>
<th>Export Classification</th>
</tr>
</thead>
<tbody>
<tr>
<td>MM1-0832HSM-2</td>
<td>3x3 mm QFN</td>
<td>SM</td>
<td>RoHS</td>
<td>Active</td>
<td>EAR99</td>
</tr>
<tr>
<td>EVAL-MM1-0832H</td>
<td>Connectorized Evaluation Fixture</td>
<td>Eval</td>
<td></td>
<td>Active</td>
<td>EAR99</td>
</tr>
</tbody>
</table>

1 Refer to our website for a list of definitions for terminology presented in this table.
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## Revision History

<table>
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<tr>
<th>Revision Code</th>
<th>Revision Date</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>-</td>
<td>January 2019</td>
<td>Datasheet Initial Release</td>
</tr>
<tr>
<td>A</td>
<td>August 2019</td>
<td>Updated Low Side LO Plot Titles</td>
</tr>
<tr>
<td>B</td>
<td>October 2020</td>
<td>Updated Pin 2 Description &amp; Equivalent Circuit</td>
</tr>
</tbody>
</table>
2. Port Configurations and Functions

2.1 Port Diagram
A bottom-up view of the MM1-0832HSM’s SM package outline drawing is shown below. The MM1-0832HSM has the input and output ports given in Port Functions. The MM1-0832HSM can be used in either an up or down conversion. For configuration A, input the LO into pin 2, use pin 8 for the RF, and pin 5 for the IF. For configuration B, input the LO into pin 8, use pin 2 for the RF, and pin 5 for the IF.

![Port Diagram](image)

2.2 Port Functions

<table>
<thead>
<tr>
<th>Port</th>
<th>Function</th>
<th>Description</th>
<th>Equivalent Circuit for Package</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pin 2</td>
<td>LO (Configuration A) RF (Configuration B)</td>
<td>Pin 2 is DC open and AC matched to 50 Ohms from 8 to 32 GHz.</td>
<td><img src="image" alt="Equivalent Circuit" /></td>
</tr>
<tr>
<td>Pin 5</td>
<td>IF</td>
<td>Pin 5 is DC coupled to the diodes. Blocking capacitor is optional.</td>
<td><img src="image" alt="Equivalent Circuit" /></td>
</tr>
<tr>
<td>Pin 8</td>
<td>RF (Configuration A) LO (Configuration B)</td>
<td>Pin 8 is DC open and AC matched to 50 Ohms from 8 to 32 GHz.</td>
<td><img src="image" alt="Equivalent Circuit" /></td>
</tr>
<tr>
<td>GND</td>
<td>Ground</td>
<td>SM package ground path is provided through the ground paddle.</td>
<td><img src="image" alt="Equivalent Circuit" /></td>
</tr>
</tbody>
</table>
3. Specifications

3.1 Absolute Maximum Ratings
The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may be inoperable or have a reduced lifetime.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Maximum Rating</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pin 2 DC Current</td>
<td>30</td>
<td>mA</td>
</tr>
<tr>
<td>Pin 5 DC Current</td>
<td>15</td>
<td>mA</td>
</tr>
<tr>
<td>Power Handling, at any Port</td>
<td>+28</td>
<td>dBm</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>-55 to +100</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65 to +125</td>
<td>°C</td>
</tr>
</tbody>
</table>

3.2 Package Information

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Details</th>
<th>Rating</th>
</tr>
</thead>
<tbody>
<tr>
<td>ESD</td>
<td>Human Body Model (HBM), per MIL-STD-750, Method 1020</td>
<td>1A</td>
</tr>
<tr>
<td>Weight</td>
<td>EVAL package</td>
<td>13.4 g</td>
</tr>
</tbody>
</table>

3.3 Recommended Operating Conditions
The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Nominal</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$T_A$, Ambient Temperature</td>
<td>-55</td>
<td>+25</td>
<td>+100</td>
<td>°C</td>
</tr>
<tr>
<td>LO Input Power</td>
<td>+16</td>
<td>+23</td>
<td>dBm</td>
<td></td>
</tr>
</tbody>
</table>

3.4 Sequencing Requirements
There is no requirement to apply power to the ports in a specific order. However, it is recommended to provide a 50Ω termination to each port before applying power. This is a passive diode mixer that requires no DC bias.
### 3.5 Electrical Specifications

The electrical specifications apply at TA=+25°C in a 50Ω system. Typical data shown is for a down conversion application with a +18dBm sine wave LO input. Specifications shown for configuration A (B).

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Test Conditions</th>
<th>Min</th>
<th>Typical</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>RF (Pin B) Frequency Range</td>
<td></td>
<td>8</td>
<td>32</td>
<td></td>
<td>GHz</td>
</tr>
<tr>
<td>LO (Pin 2) Frequency Range</td>
<td></td>
<td>8</td>
<td>30</td>
<td></td>
<td>GHz</td>
</tr>
<tr>
<td>I (Pin 5) Frequency Range</td>
<td></td>
<td>0</td>
<td>12</td>
<td></td>
<td>GHz</td>
</tr>
<tr>
<td>Conversion Loss (CL)$^2$</td>
<td>RF/LO = 8 - 30 GHz I = DC - 0.2 GHz</td>
<td>7</td>
<td>10.5</td>
<td>36</td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td>RF/LO = 8 - 30 GHz I = 0.2 - 12 GHz</td>
<td>8</td>
<td>12</td>
<td>8</td>
<td>dB</td>
</tr>
<tr>
<td>Noise Figure (NF)$^3$</td>
<td>RF/LO = 8 - 30 GHz I = DC - 0.2 GHz</td>
<td>8.5</td>
<td></td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>Isolation</td>
<td>LO to RF RF/LO = 8 - 32 GHz</td>
<td>36</td>
<td></td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td>LO to IF IF/LO = 8 - 32 GHz</td>
<td>23</td>
<td></td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td>RF to IF RF/IF = 8 - 32 GHz</td>
<td>23</td>
<td></td>
<td></td>
<td>dB</td>
</tr>
<tr>
<td>Input IP3 (IIP3)</td>
<td>RF/LO = 8 - 32 GHz I = DC - 0.2 GHz</td>
<td>+20.5</td>
<td>(+22)</td>
<td></td>
<td>dBm</td>
</tr>
<tr>
<td>Input 1 dB Gain Compression Point (P1dB)</td>
<td>RF/LO = 8 - 32 GHz I = DC - 0.2 GHz</td>
<td>+9</td>
<td>(+11)</td>
<td></td>
<td>dBm</td>
</tr>
</tbody>
</table>

---

$^2$ Measured as a down converter to a fixed 91MHz IF.

$^3$ Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.
3.6 Typical Performance Plots

**Conversion Loss: 91 MHz IF, LSLO (dB)**

- RF Frequency (GHz)
- Configuration A
- Configuration B

**Conversion Loss: 5 GHz IF, Low Side LO (dB)**

- RF Frequency (GHz)
- Configuration A
- Configuration B

**Configuration A Conversion Loss vs. LO Power: 91 MHz IF (dB)**

- RF Frequency (GHz)
- +22 dBm
- +20 dBm
- +18 dBm
- +16 dBm
- +14 dBm

**Config A Conversion Loss vs. LO Power: 5 GHz IF (dB) Low Side LO**

- RF Frequency (GHz)
- +22 dBm
- +20 dBm
- +18 dBm
- +16 dBm
- +14 dBm

**Configuration B Conversion Loss vs. LO Power: 91 MHz IF (dB)**

- RF Frequency (GHz)
- +22 dBm
- +20 dBm
- +18 dBm
- +16 dBm
- +14 dBm

**Config B Conversion Loss vs. LO Power: 5 GHz IF (dB) Low Side LO**

- RF Frequency (GHz)
- +22 dBm
- +20 dBm
- +18 dBm
- +16 dBm
- +14 dBm

**Relative IF Response (dB)**

- IF Frequency (GHz)
- 9 GHz RF - Configuration A
- 9 GHz RF - Configuration B

- 29 GHz RF - Configuration A
- 29 GHz RF - Configuration B
3.6.1 Typical Performance Plots: IP3

Input IP3 (dBm)

RF Frequency (GHz)

Output IP3 (dBm)

RF Frequency (GHz)

Configuration A Input IP3 vs LO Power (dBm)

 RF Frequency (GHz)

Configuration B Input IP3 vs LO Power (dBm)

 RF Frequency (GHz)

Configuration A Output IP3 vs LO Power (dBm)

 RF Frequency (GHz)

Configuration B Output IP3 vs LO Power (dBm)

 RF Frequency (GHz)
3.6.2 Typical Performance Plots: LO Harmonic Isolation

Even LO Harmonic to RF Isolation (dB)

Even LO Harmonic to IF Isolation (dB)

Odd LO Harmonic to RF Isolation (dB)

Odd LO Harmonic to IF Isolation (dB)

2RF x 2LO Spurious Suppression (dBc) -10 dBm RF Input

2IF x 1LO Spurious Suppression (dBc) -10 dBm IF Input
3.6.3 Typical Spurious Performance: Down-Conversion

Typical spurious data is provided by selecting RF and LO frequencies (± m*LO ± n*RF) within the RF/LO bands, to create a spurious output within the IF band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by (n-1), where “n” is the RF spur order. For example, the 2RF x 2LO spur is 61 dBc for a -10 dBm input, so a -20 dBm RF input creates a spur that is (2-1) x (-10 dB) lower, or 71 dBc. Data is shown for the frequency plan in 3.6 Typical Performance. mLOxRF plots can be found in section 3.6.2 Typical Performance Plots: LO Harmonic Isolation. OLOx1RF plot is identical to the plot of LO-RF isolation.

**Typical Down-conversion spurious suppression (dBc): Config A (B)**

<table>
<thead>
<tr>
<th>-10 dBm RF Input</th>
<th>0xLO</th>
<th>1xLO</th>
<th>2xLO</th>
<th>3xLO</th>
<th>4xLO</th>
<th>5xLO</th>
</tr>
</thead>
<tbody>
<tr>
<td>0xF</td>
<td>-</td>
<td>26 (25)</td>
<td>37 (32)</td>
<td>43 (46)</td>
<td>51 (47)</td>
<td>75 (64)</td>
</tr>
<tr>
<td>1xF</td>
<td>17 (11) Reference</td>
<td>29 (36)</td>
<td>13 (15)</td>
<td>78 (54)</td>
<td>81 (73)</td>
<td></td>
</tr>
<tr>
<td>2xF</td>
<td>72 (71)</td>
<td>59 (49)</td>
<td>61 (64)</td>
<td>60 (53)</td>
<td>64 (67)</td>
<td>56 (54)</td>
</tr>
<tr>
<td>3xF</td>
<td>74 (78)</td>
<td>59 (63)</td>
<td>79 (86)</td>
<td>71 (75)</td>
<td>78 (86)</td>
<td>66 (75)</td>
</tr>
<tr>
<td>4xF</td>
<td>108 (103)</td>
<td>91 (89)</td>
<td>105 (108)</td>
<td>107 (106)</td>
<td>107 (109)</td>
<td>107 (104)</td>
</tr>
<tr>
<td>5xF</td>
<td>118 (120)</td>
<td>108 (95)</td>
<td>112 (120)</td>
<td>119 (121)</td>
<td>119 (123)</td>
<td>117 (123)</td>
</tr>
</tbody>
</table>

3.6.4 Typical Spurious Performance: Up-Conversion

Typical spurious data is taken by mixing an input within the IF band, with LO frequencies (± m*LO ± n*IF), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by (n-1), where “n” is the IF spur order. For example, the 2IF x 1LO spur is typically 63 dBc for a -10 dBm input with a sine-wave LO, so a -20 dBm IF input creates a spur that is (2-1) x (-10 dB) lower, or 73 dBc. Data is shown for the frequency plan in 3.6 Typical Performance.

**Typical Up-conversion spurious suppression (dBc): Config A (B)**

<table>
<thead>
<tr>
<th>-10 dBm RF Input</th>
<th>0xIF</th>
<th>1xIF</th>
<th>2xIF</th>
<th>3xIF</th>
<th>4xIF</th>
<th>5xIF</th>
</tr>
</thead>
<tbody>
<tr>
<td>0xIF</td>
<td>-</td>
<td>51 (50)</td>
<td>54 (48)</td>
<td>36 (35)</td>
<td>65 (57)</td>
<td>69 (53)</td>
</tr>
<tr>
<td>1xIF</td>
<td>23 (22) Reference</td>
<td>32 (39)</td>
<td>14 (14)</td>
<td>34 (39)</td>
<td>25 (36)</td>
<td></td>
</tr>
<tr>
<td>2xIF</td>
<td>80 (59)</td>
<td>63 (64)</td>
<td>56 (46)</td>
<td>57 (66)</td>
<td>49 (64)</td>
<td>57 (58)</td>
</tr>
<tr>
<td>3xIF</td>
<td>97 (102)</td>
<td>67 (70)</td>
<td>75 (86)</td>
<td>67 (67)</td>
<td>74 (83)</td>
<td>55 (61)</td>
</tr>
<tr>
<td>4xIF</td>
<td>117 (113)</td>
<td>106 (112)</td>
<td>95 (88)</td>
<td>100 (105)</td>
<td>87 (91)</td>
<td>98 (107)</td>
</tr>
<tr>
<td>5xIF</td>
<td>131 (132)</td>
<td>112 (114)</td>
<td>116 (121)</td>
<td>104 (97)</td>
<td>121 (125)</td>
<td>101 (104)</td>
</tr>
</tbody>
</table>
4. Operation

4.1 Application Circuit

4.2 Ports Operation

IF Port — Used as input on an upconversion, output on downconversion, or LO port in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads. Blocking capacitor is recommended if DC voltage is present on the line.

RF Port — Used as input on a downconversion, output on upconversion, or output in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads.

Filtering and Matching - Filtering is generally desired for spurious and image removal on the output port of the mixer. Reflective filters can cause out of band signals to reflect back into the mixer and cause conversion loss ripple, erroneous spurs, and other undesired behaviors. To eliminate these problems it is recommend that the filters be placed as close to the output port as possible. If undesired behavior is still observed, a diplexer with one port terminated or a 1-3 dB attenuator may reduce this problem.

RF Ground — The ground paddle of the QFN should be connected to a low noise RF ground with very low electrical resistance for high frequency operation.

LO Port — The noise floor of the LO input signal should be less than the value of the noise floor plus isolation of the mixer, or a filter is recommended to prevent reduction in dynamic range. An LO amplifier is required if the LO power is below the recommended drive level. It is important to use an amplifier with a broadband 50 ohm match such that it does not reflect spurious signals back into the mixer or other system circuitry.
5. Mechanical Data

5.1 SM Package Outline Drawing

1. Substrate material is ceramic.
2. I/O Leads and Ground Paddle plating is (from base to finish):
   - Ni: 8.99um MAX 1.27um MIN
   - Pd: 0.17um MAX 0.07um MIN
   - Au: 0.254um MAX 0.03um MIN
3. All unconnected pads should be connected to PCB RF ground.

5.2 SM Package Footprint

Click here for a DXF of the above layout.

Click here for leaded solder reflow.  Click here for lead-free solder reflow.
5.3 Evaluation Board Outline Drawing

<table>
<thead>
<tr>
<th>Port</th>
<th>Connector Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>LO</td>
<td>2.92 mm Female</td>
</tr>
<tr>
<td>RF</td>
<td>2.92 mm Female</td>
</tr>
<tr>
<td>IF</td>
<td>SMA Female</td>
</tr>
</tbody>
</table>

Note: Eval Connectors are not removable.