

## MICROLITHIC™ DOUBLE-BALANCED MIXER

## ML1-0732

The ML1-0732 is a Microlithic™ double balanced mixer. As with all Microlithic™ mixers (patent pending), it features excellent conversion loss, isolations, and spurious performance across a broad bandwidth and in a miniaturized form factor. Accurate, nonlinear software models are available for Microwave Office through the Marki Microwave PDK. The ML1-0732 is available as a wire bondable chip or in a connectorized package. The ML1-0732 is an excellent alternative to Marki Microwave M9 mixers packaged in drop-in carriers such as the ES carrier.



### Features

- Compact Chip Style Package (0.152" x 0.090" x 0.015")
- CAD Optimized for Superior Isolation and Spurious Response
- Broadband Performance
- Excellent Unit-to-Unit Repeatability
- Fully nonlinear software models available with Marki PDK for Microwave Office

**Electrical Specifications** - Specifications guaranteed from -55 to +100°C, measured in a 50Ω system.

Parameter	LO (GHz)	RF (GHz)	IF (GHz)	Min	Typ	Max	Diode Option LO drive level (dBm)	
Conversion Loss (dB)	7-32		DC-6		8	11		
			6-8		11	14		
Isolation (dB)								
LO-RF						42		
LO-IF						25		
RF-IF				35				
Input 1 dB Compression (dBm)					+3		L (+10 to +13)	
					+9		I (+15 to +19)	
Input Two-Tone Third Order Intercept Point (dBm)					+14		L (+10 to +13)	
					+21		I (+15 to +19)	

### Part Number Options<sup>1</sup>

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification	Recommended Part Number Option 1	Recommended Part Number Option 2
ML1-0732LCH-2	Chip	CH	RoHS Compliant	Active Not Recommended for New Designs	EAR99	<a href="#">MM1-0626HCH-2</a>	MM1-0726LCH-2 (coming soon)
ML1-0732ICH-2	Chip	CH				<a href="#">MM1-0626HCH-2</a>	<a href="#">MM1-1140HCH-2</a>
ML1-0732LS	Connectorized	S				<a href="#">MM1-0626HS</a>	MM1-0726LS (coming soon)
ML1-0732IS	Connectorized	S				<a href="#">MM1-0626HS</a>	<a href="#">MM1-1140HS</a>

<sup>1</sup>Refer to our [website](#) for a list of definitions for terminology presented in this table.

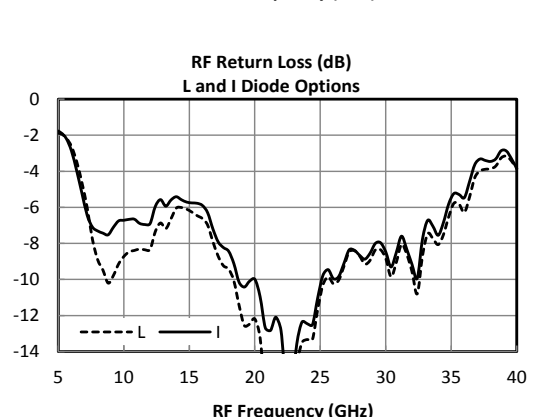
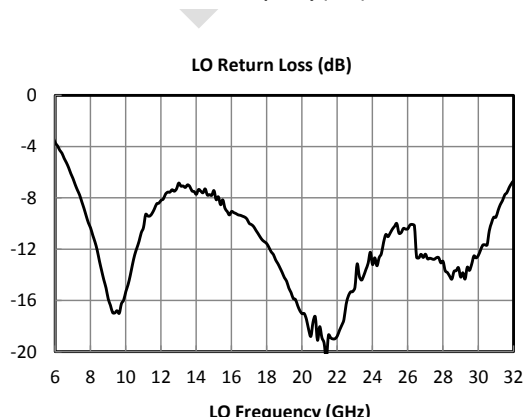
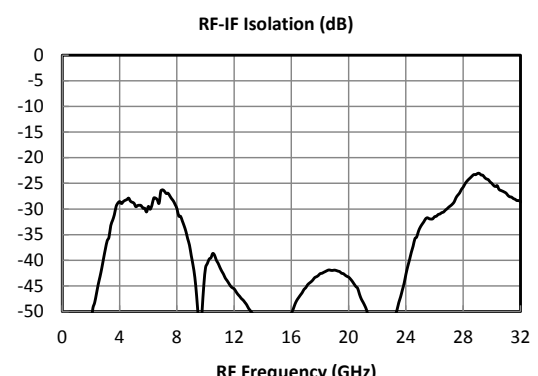
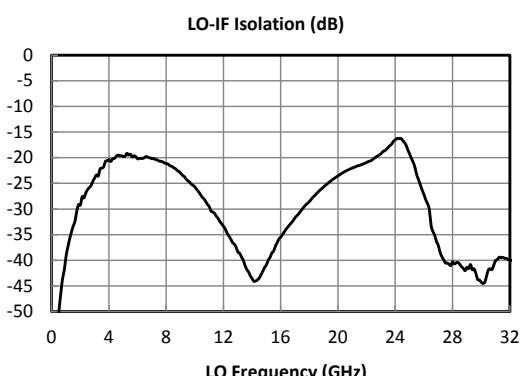
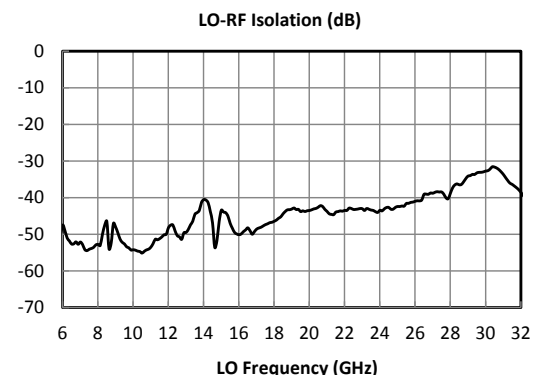
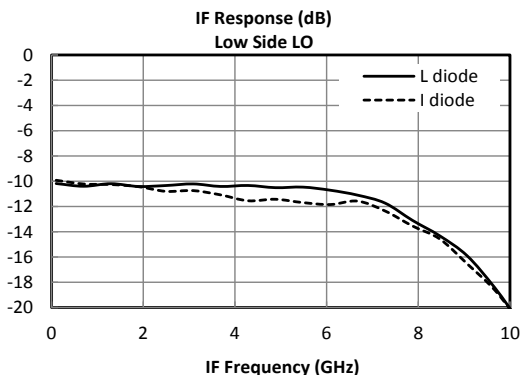
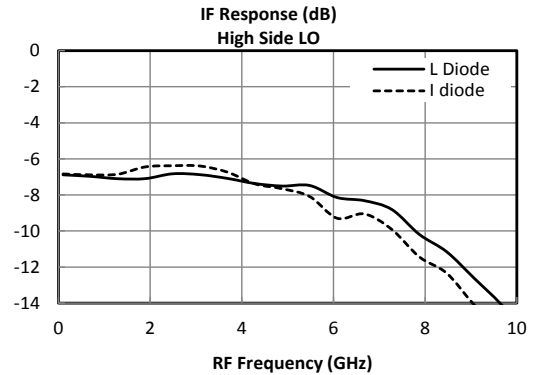
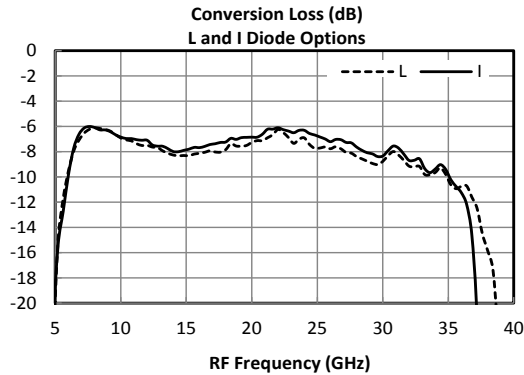
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LO/RF 7 to 32 GHz  
IF DC to 8 GHz

### Typical Performance



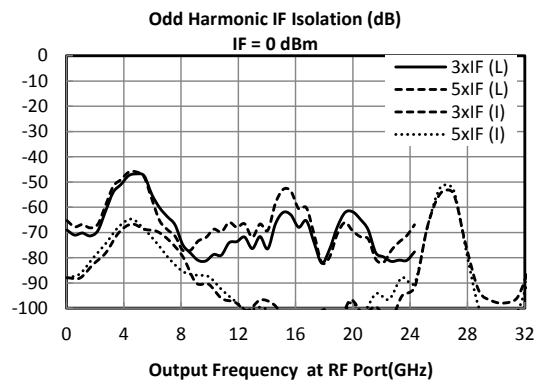
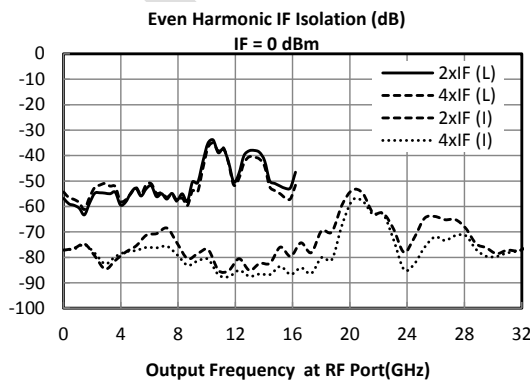
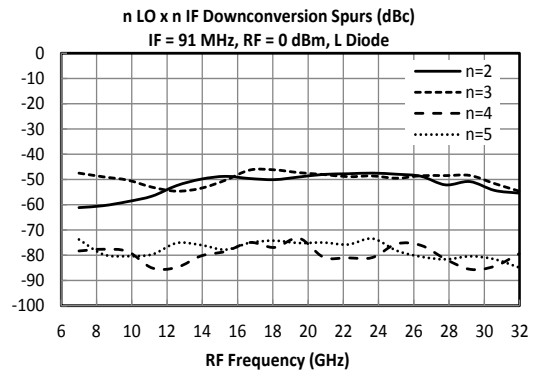
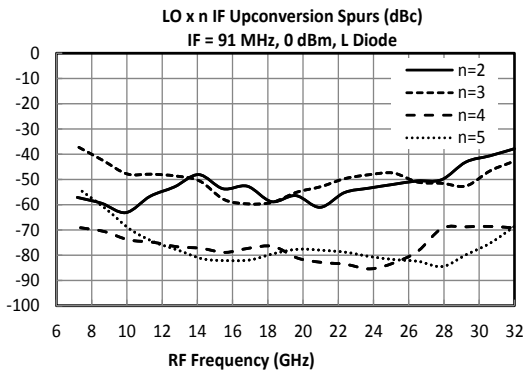
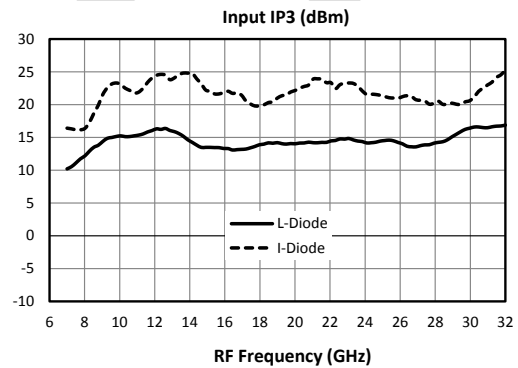
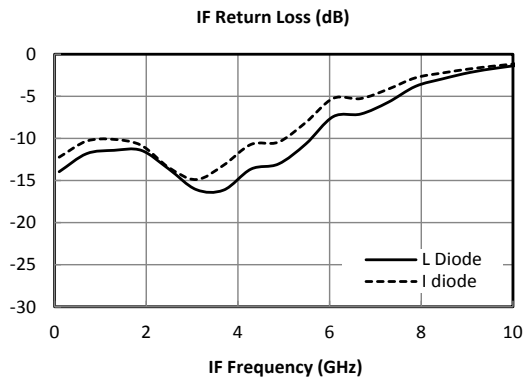
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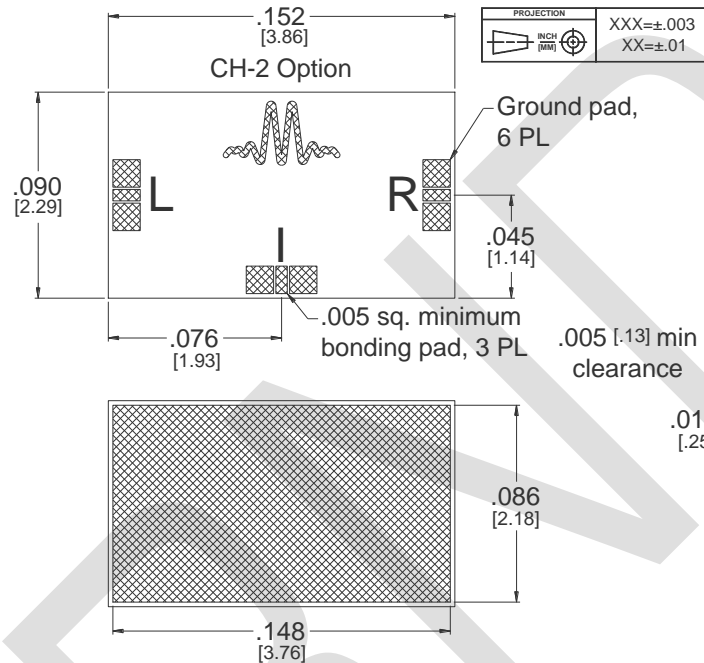


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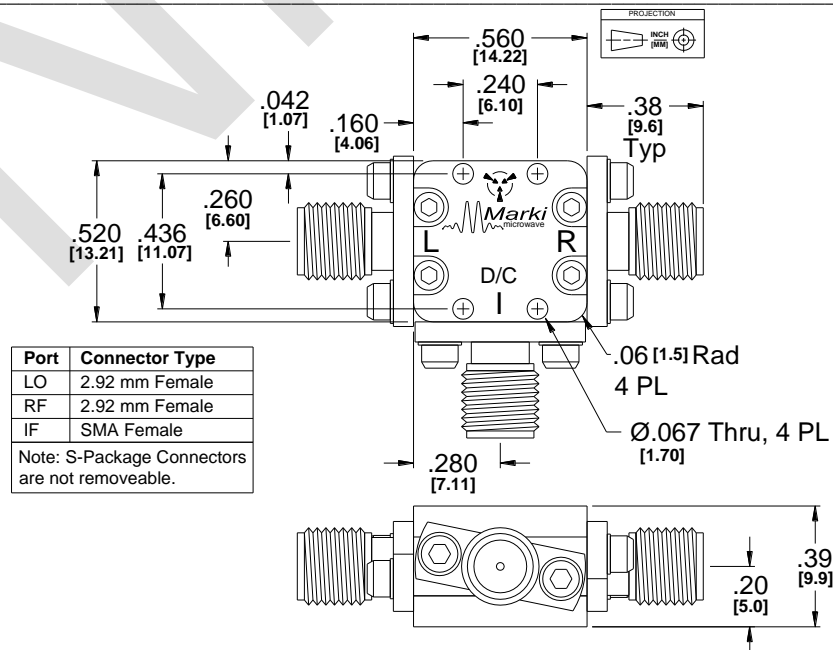


Outline Drawing – CH-2 package

\*CH Substrate material is .010 thick Ceramic.

I/O traces and ground plane finish is 2.5 microns Au over .05 microns WTi.

Wire Bonding - Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. All bonds should be as short as possible <0.31 mm (12 mils).



Outline Drawing – S package



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**LO/RF 7 to 32 GHz  
IF DC to 8 GHz**

**Downconversion Spurious Suppression**

Spurious data is taken by selecting RF and LO frequencies ( $\pm mLO \pm nRF$ ) within the RF/LO bands, to create a spurious output within the IF band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by  $(n-1)$ , where “n” is the RF spur order. For example, the  $2RF \times 2LO$  spur is 62 dBc for a -10 dBm input, so a -20 dBm RF input creates a spur that is  $(2-1) \times (-10 \text{ dB})$  dB lower, or 72 dBc.

**Typical Downconversion Spurious Suppression (dBc): I diode (L diode) <sup>5</sup>**

<b>-10 dBm RF Input</b>	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	Reference	30(39)	11(10)	30(43)	24(22)
2xRF	51(52)	62(61)	60(55)	62(61)	62(56)
3xRF	59(49)	84(79)	70(58)	89(79)	69(56)
4xRF	101(84)	105(97)	107(94)	110(101)	106(94)
5xRF	120(97)	116(105)	119(102)	119(115)	118(100)

**Upconversion Spurious Suppression**

Spurious data is taken by mixing an input within the IF band, with LO frequencies ( $\pm mLO \pm nIF$ ), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by  $(n-1)$ , where “n” is the IF spur order. For example, the  $2IF \times 1LO$  spur is typically 61 dBc for a -10 dBm input, so a -20 dBm IF input creates a spur that is  $(2-1) \times (-10 \text{ dB})$  dB lower, or 71 dBc.

**Typical Upconversion Spurious Suppression (dBc): I diode (L diode) <sup>5</sup>**

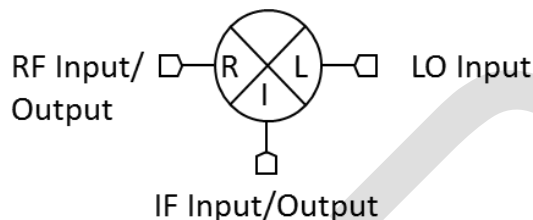
<b>-10 dBm IF Input</b>	1xLO	2xLO	3xLO	4xLO	5xLO
1xIF	Reference	32(38)	12(10)	43(41)	28(20)
2xIF	61(63)	53(58)	59(70)	44(47)	57(56)
3xIF	71(70)	81(88)	64(64)	76(76)	61(51)
4xIF	106(106)	97(100)	101(105)	83(86)	86(88)
5xIF	117(116)	119(126)	107(110)	110(113)	84(87)



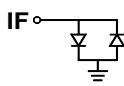
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LO/RF 7 to 32 GHz  
IF DC to 8 GHz



Port	Description	DC Interface Schematic
LO	The LO port is DC short to ground and AC matched to 50 Ohms from 7 GHz to 32 GHz. Blocking capacitor is optional.	
RF	The RF port is DC short to ground and AC matched to 50 Ohms from 7 GHz to 32 GHz. Blocking capacitor is optional.	
IF	The IF port is DC coupled to the diodes. Blocking capacitor is optional.	

Absolute Maximum Ratings	
Parameter	Maximum Rating
RF DC Current	1 Amp
LO DC Current	1 Amp
IF DC Current	50 mA
RF Power Handling (RF+LO)	+25 dBm at +25°C, derated linearly to +20 dBm at +100°C
Operating Temperature	-55°C to +100°C
Storage Temperature	-65°C to +125°C

**DATA SHEET NOTES:**

- Mixer Conversion Loss Plot IF frequency is 100 MHz.
- Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.
- Conversion Loss typically degrades less than 0.5 dB for LO drives 2 dB below the lowest and 3 dB above highest nominal LO drive levels.
- Conversion Loss typically degrades less than 0.5 dB at +100°C and improves less than 0.5 dB at -55°C.
- Unless otherwise specified L diode data taken with 13 dBm LO drive, I diode data taken with 17 dBm LO drive.
- Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.
- Catalog mixer circuits are continually improved. Configuration control requires custom mixer model numbers and specifications.

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