

GaAs MMIC Double Balanced Mixer

MM1-0626HSM

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The MM1-0626HSM is a passive GaAs double balanced MMIC mixer suitable for both up and down-conversion applications. As with all Marki Microwave mixers, it features excellent conversion loss, isolation and spurious performance across a broad bandwidth and in a small form factor. The MM1-0626HSM is available in a lead-free, RoHS compliant QFN surface mount package and is compatible with standard leaded and lead-free PCB reflow soldering processes. The MM1-0626HSM is a superior alternative to Marki Microwave surface mount M1 and M3 mixers.



Features

- Compact 3mm QFN SMT Style Package
- Broadband Performance
- Excellent Unit-to-Unit Repeatability
- RoHS Compliant

Electrical Specifications - Specifications guaranteed from -55 to +100°C, measured in a 50Ω system. Specifications are shown for Configurations A & B. See page 2 for port locations.

Parameter	LO (GHz)	RF (GHz)	IF (GHz)	Min	Typ	Max	LO drive level (dBm)
Conversion Loss (dB)	6-26.5	6-26.5	DC-9		7.5 (9)		+15 (+15)
Isolation (dB)							
LO-RF				47			
LO-IF				34			
RF-IF				35			Config. A: + 13 to + 20 Config. B: + 13 to + 20
Input 1 dB Compression (dBm)				+9 (+9)			
Input Two-Tone Third Order Intercept Point (dBm) ²				+21 (+23)			

Part Number Options

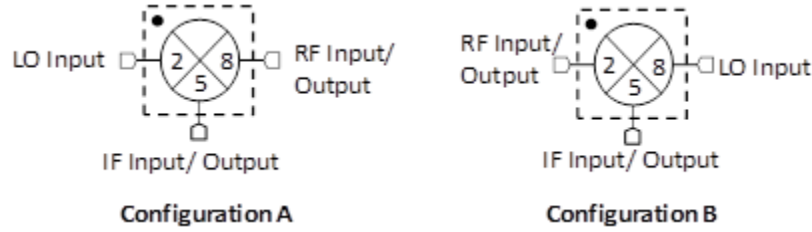
Model Number	Description
MM1-0626HSM-2 ¹	Surface Mount, IF Port Configuration -2
EVAL-MM1-0626H	Connectorized Evaluation Fixture

¹ Note: For port locations and I/O designations, refer to the drawings on page 2 of this document.

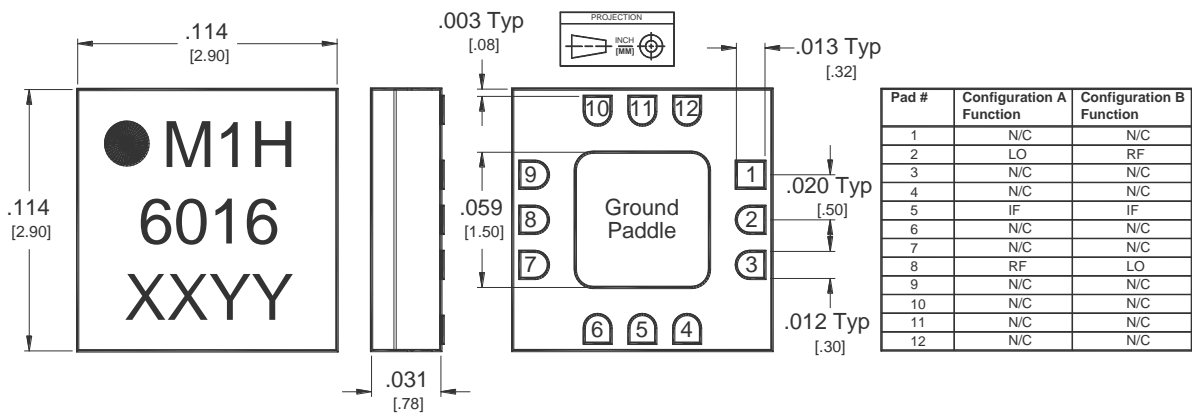
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1. Configuration A/B refer to the same part number (MM1-0626H) used in one of two different ways for optimal spurious performance. For the lowest conversion loss, use the mixer in Configuration A (port 1 as the LO input, port 2 as the RF input or output). If you need to use a lower LO drive, use the mixer in Configuration B (port 1 as the RF input or output, port 2 as the LO input). For optimal spurious suppression, experimentation or simulation is required to choose between Configuration A and B. For more information, [see here](#).

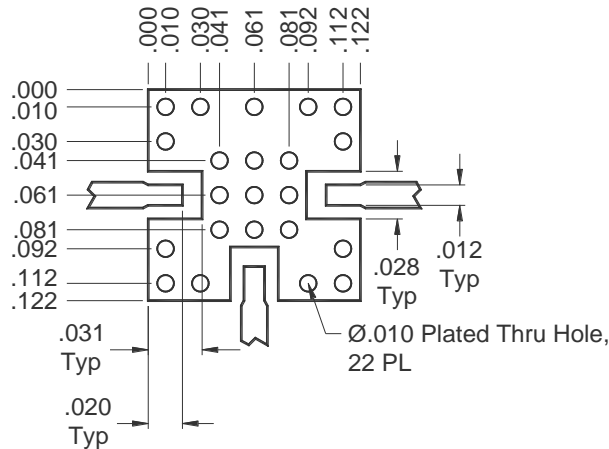


Outline Drawing – 3mm QFN package

- Substrate material is ceramic.
- I/O Leads and Ground Paddle plating is (from base to finish):

Ni:	8.89um MAX	1.27um MIN
Pd:	0.17um MAX	0.07um MIN
Au	0.254um MAX	0.03um MIN

All unconnected pads should be connected to PCB RF ground



QFN-Package Surface-Mount Landing Pattern

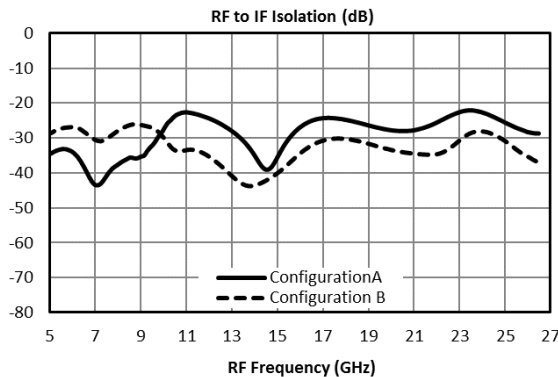
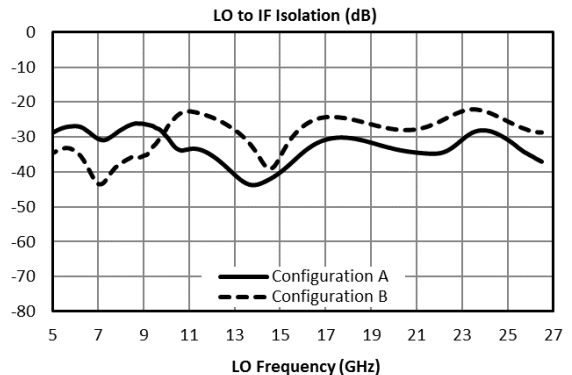
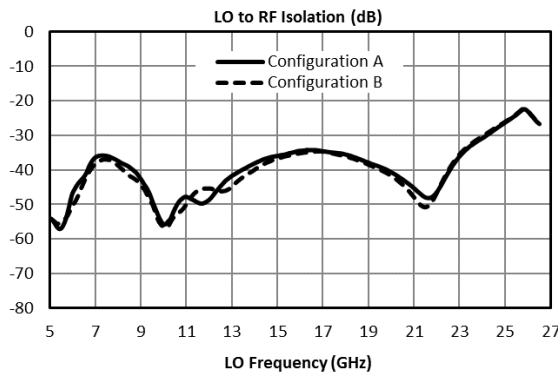
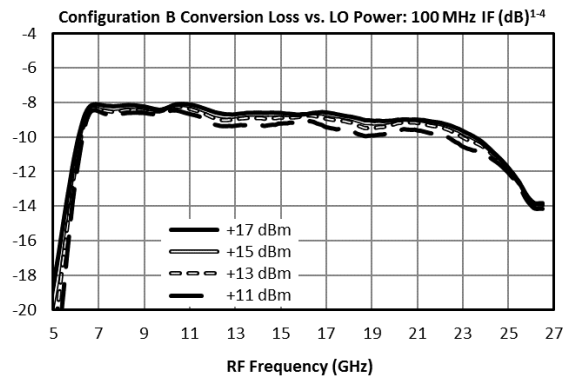
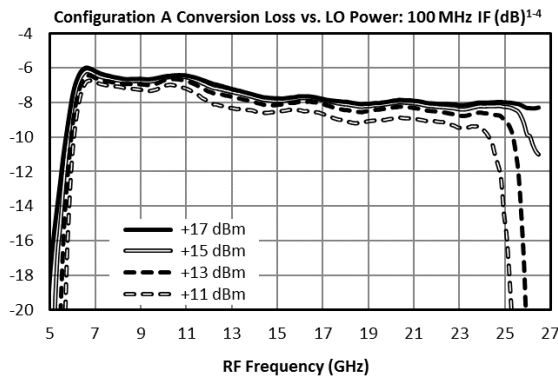
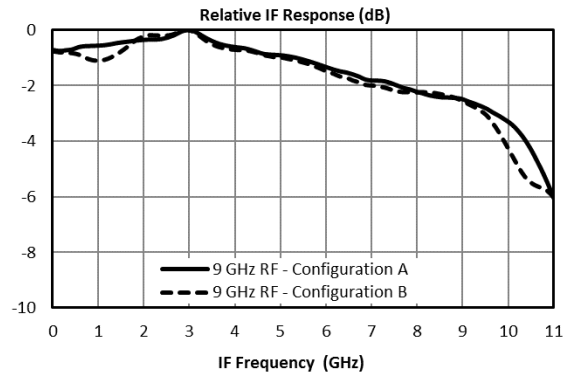
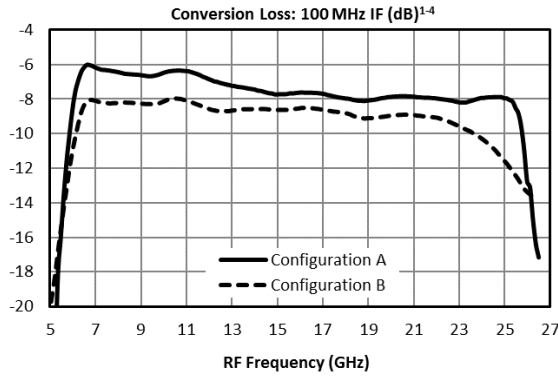
[Click here for a DXF of the above layout.](#)
[Click here for leaded solder reflow.](#) [Click here for lead-free solder reflow.](#)

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Typical Performance

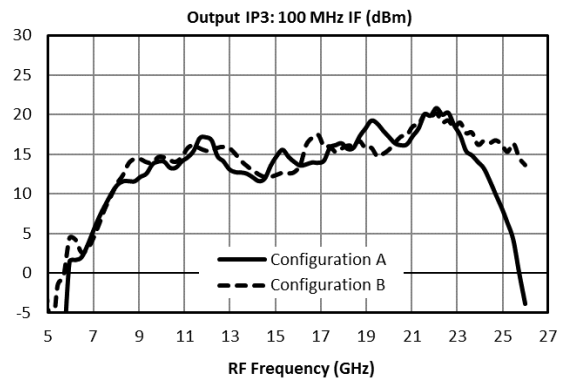
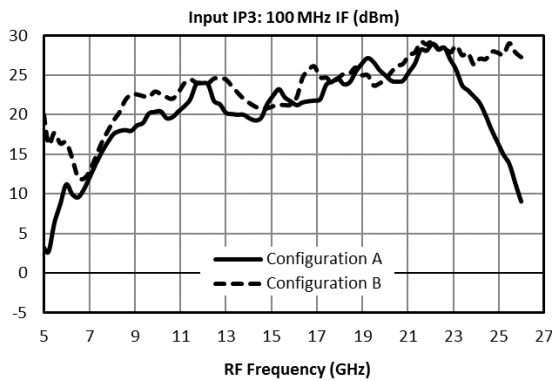
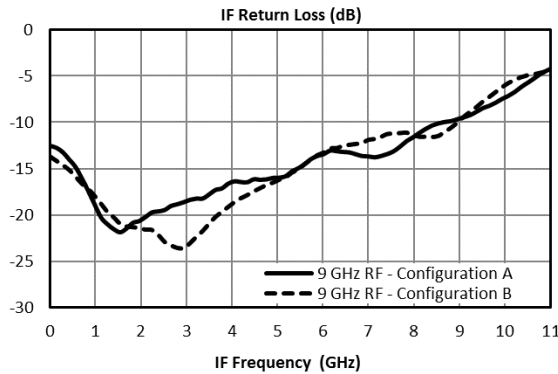
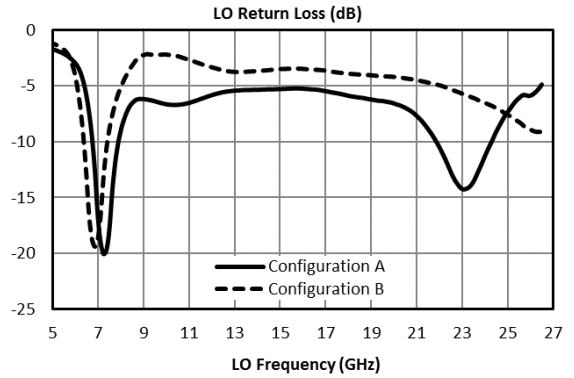
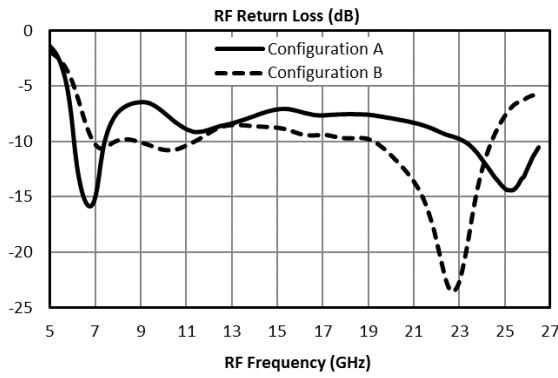


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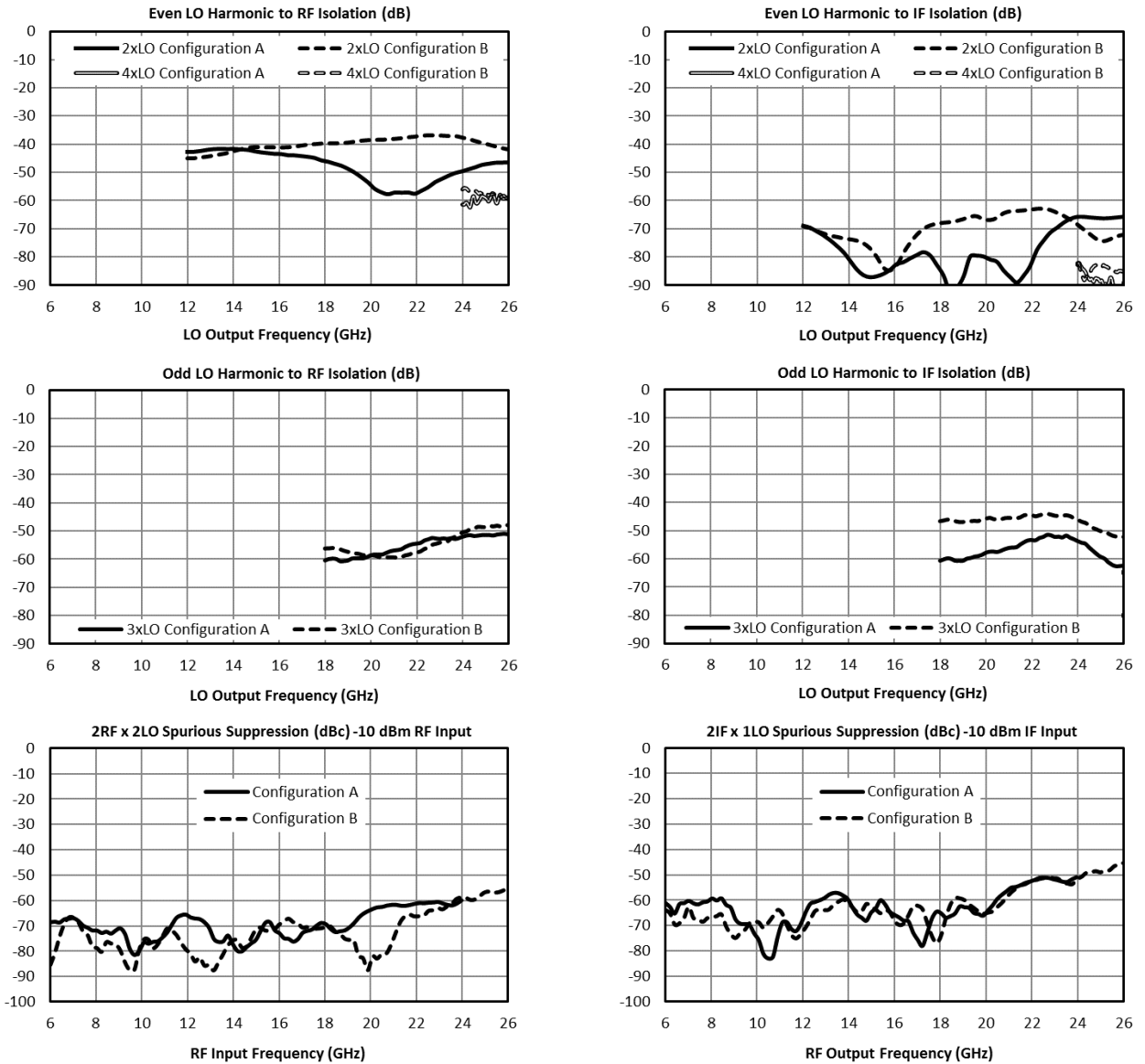
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Typical Performance



Typical Performance





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Downconversion Spurious Suppression

Spurious data is taken by selecting RF and LO frequencies ($\pm mLO \pm nRF$) within the 6 to 26 GHz RF/LO bands, which create a 91 MHz IF spurious output. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by (n-1), where “n” is the RF spur order. For example, the 2RFx2LO spur is 70 dBc for the A configuration for a -10 dBm input, so a -20 dBm RF input creates a spur that is (2-1) x (-10 dB) dB lower, or 80 dBc.

Typical Downconversion Spurious Suppression (dBc): A Configuration (B Configuration), Sine Wave LO ⁵

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	23 (24)	Reference	31 (35)	13 (12)	36 (43)	14 (17)
2xRF	60 (63)	63 (55)	70 (73)	65 (57)	73 (72)	70 (69)
3xRF	111 (112)	62 (64)	83 (92)	73 (80)	83 (93)	68 (72)
4xRF	120 (128)	107 (102)	108 (119)	108 (108)	112 (117)	109 (108)
5xRF	N/A	112 (134)	122 (133)	117 (126)	127 (132)	115 (124)

Upconversion Spurious Suppression

Spurious data is taken by mixing a 91 MHz IF with LO frequencies ($\pm mLO \pm nIF$), which creates an RF within the 6 to 26 GHz RF band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by (n-1), where “n” is the IF spur order. For example, the 2IFx1LO spur is typically 63 dBc for the A configuration for a -10 dBm input, so a -20 dBm IF input creates a spur that is (2-1) x (-10 dB) dB lower, or 73 dBc.

Typical Upconversion Spurious Suppression (dBc): A Configuration (B Configuration), Sine Wave LO ⁵

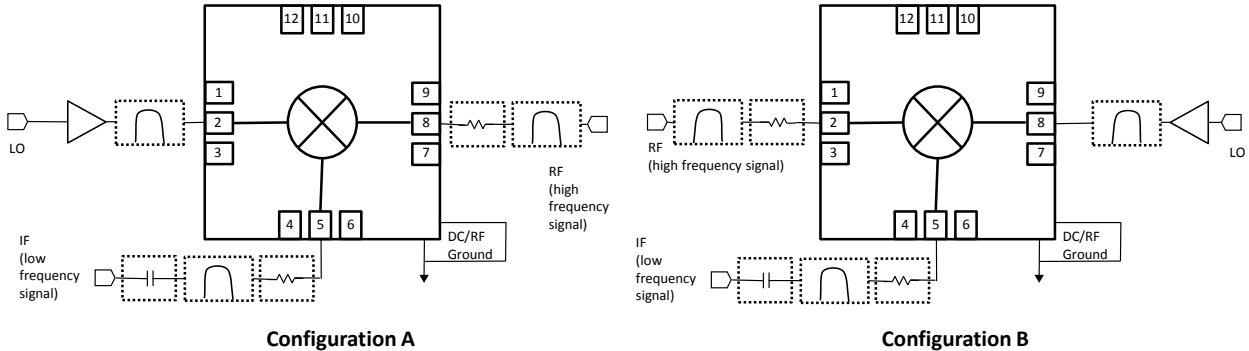
-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xIF	30 (19)	Reference	33 (38)	11 (10)	32 (48)	21 (23)
2xIF	65 (57)	63 (62)	61 (54)	65 (68)	61 (60)	65 (60)
3xIF	111 (111)	67 (70)	77 (87)	61 (64)	78 (91)	62 (64)
4xIF	107 (102)	111 (116)	102 (97)	112 (116)	105 (99)	109 (108)
5xIF	128 (144)	114 (116)	120 (127)	115 (118)	122 (130)	107 (110)

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Application Circuit



Operation

IF Port – Used as input on an upconversion, output on downconversion, or LO port in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads. Blocking capacitor is recommended if DC voltage is present on the line.

RF Port – Used as input on a downconversion, output on upconversion, or output in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads.

Filtering and Matching- Filtering is generally desired for spurious and image removal on the output port of the mixer. Reflective filters can cause out of band signals to reflect back into the mixer and cause conversion loss ripple, erroneous spurs, and other undesired behaviors. To eliminate these problems it is recommend that the filters be placed as close to the output port as possible. If undesired behavior is still observed, a diplexer with one port terminated or a 1-3 dB attenuator may reduce this problem.

RF Ground – The ground paddle of the QFN should be connected to a low noise RF ground with very low electrical resistance for high frequency operation.

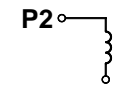
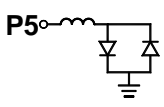
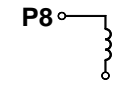
LO Port – The noise floor of the LO input signal should be less than the value of the noise floor plus isolation of the mixer, or a filter is recommended to prevent reduction in dynamic range. An LO amplifier is required if the LO power is below the recommended drive level. It is important to use an amplifier with a broadband 50 ohm match such that it does not reflect spurious signals back into the mixer or other system circuitry.

Recommended LO Amplifier		
Package	Diode Option	Amplifier
SM	H	ADM-0126-5835SM

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Port	Description	DC Interface Schematic
Port 2	Port 2 is DC open and AC matched to 50 Ohms from 6 to 26.5 GHz. Blocking capacitor is optional.	
Port 5	Port 5 is DC coupled to the diodes. Blocking capacitor is optional.	
Port 8	Port 8 is DC open and AC matched to 50 Ohms from 6 to 26.5 GHz. Blocking capacitor is optional.	

Absolute Maximum Ratings	
Parameter	Maximum Rating
Port 2 DC Current	N/A
Port 5 DC Current	15 mA
Port 8 DC Current	N/A
RF Power Handling (RF+LO)	+25 dBm at +25°C, derated linearly to +21 dBm at +100°C
Operating Temperature	-55°C to +100°C
Storage Temperature	-65°C to +125°C

DATA SHEET NOTES:

- Mixer Conversion Loss Plot IF frequency is 100 MHz.
- Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.
- Conversion Loss typically degrades less than 0.5 dB at +100°C and improves less than 0.5 dB at -55°C.
- Unless otherwise specified, data is taken with +15 dBm lowside LO drive.
- Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.
- Catalog mixer circuits are continually improved. Configuration control requires custom mixer model numbers and specifications.

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.



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Revision History

Revision Code	Revision Date	Comment
A	November 2017	Change to internal device.
B	October 2018	Change to product image on page 1

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