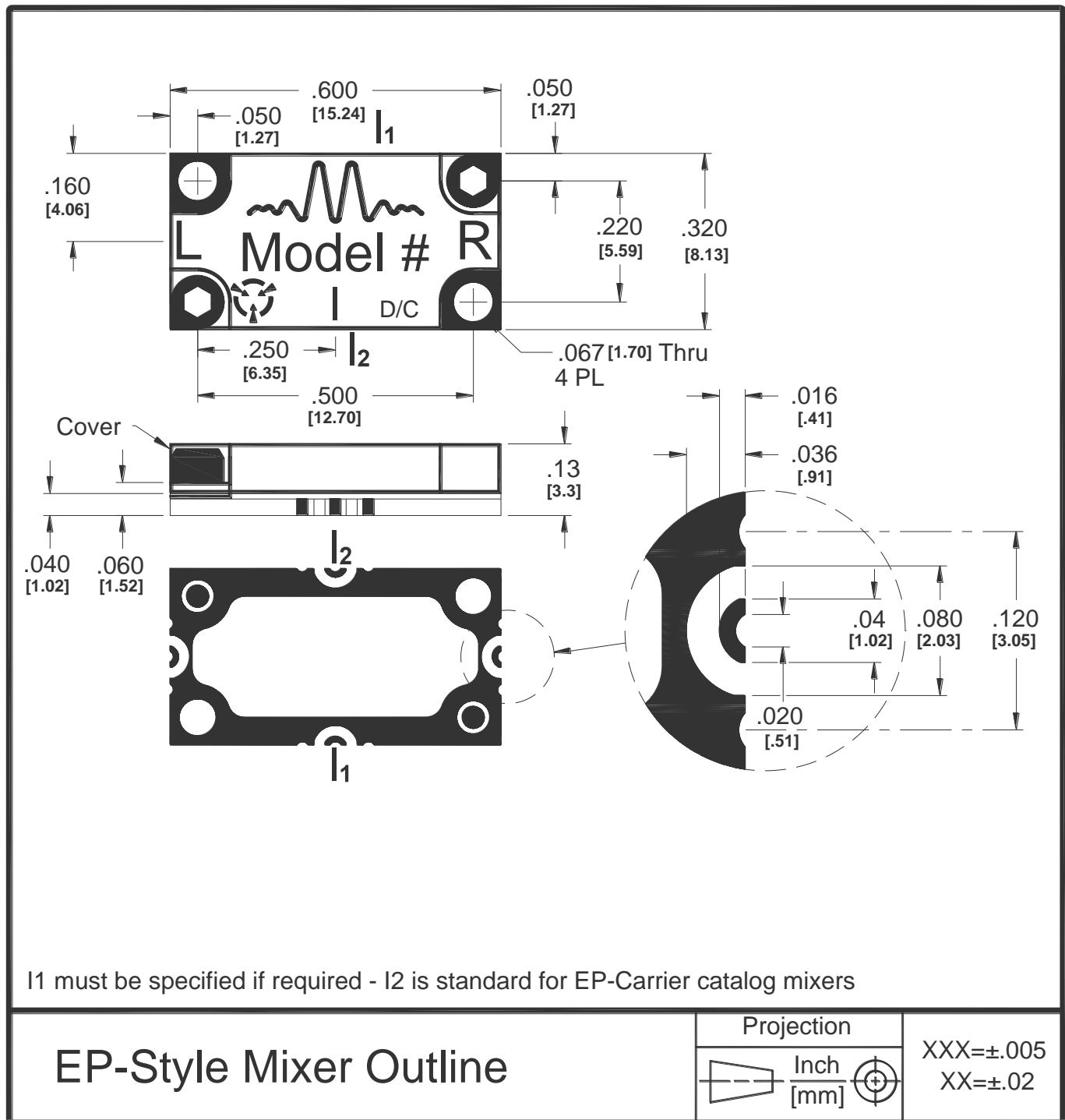




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EP Substrate material is 31-mil thick FR4, 1.0 Oz Electrodeposited Cu.  
 EP I/O castellations and ground plane finish is 100-300 μ-inches 60/40 Pb/Sn Solder Plate over Cu.  
 Click on links for [suggested PCB layout](#) and [DXF file](#).